Predictable Verification using Intrinsic Definitions

ADITHYA MURALI, University of Illinois Urbana-Champaign, Department of Computer Science, USA
CODY RIVERA, University of Illinois Urbana-Champaign, Department of Computer Science, USA
P. MADHUSUDAN, University of Illinois Urbana-Champaign, Department of Computer Science, USA

We propose a novel mechanism of defining data structures using *intrinsic definitions* that avoids recursion and instead utilizes *monadic maps satisfying local conditions*. We show that intrinsic definitions are a powerful mechanism that can capture a variety of data structures naturally. We show that they also enable a predictable verification methodology that allows engineers to write ghost code to update monadic maps and perform verification using reduction to decidable logics. We evaluate our methodology using Boogie and prove a suite of data structure manipulating programs correct.

ACM Reference Format:
Adithya Murali, Cody Rivera, and P. Madhusudan. 2024. Predictable Verification using Intrinsic Definitions. 1, 1 (March 2024), 42 pages. https://doi.org/10.1145/nnnnnnn.nnnnnnn

1 INTRODUCTION

In computer science in general, and program verification in particular, classes of finite structures (such as data structures) are commonly defined using *recursive definitions (aka inductive definitions)*. Proving that a set of structures is in such a class or proving that structures in the class have a property is naturally performed using *induction*, typically mirroring the recursive structure in its definition. For example, trees in pointer-based heaps can be defined using the following recursive definition in first-order logic (FOL) with least fixpoint semantics for definitions:

$$\begin{aligned} tree(x) ::=_{lfp}\ & x = nil \vee \big(x \neq nil \wedge tree(l(x)) \wedge tree(r(x)) \\ & \wedge x \notin htree(l(x)) \wedge x \notin htree(r(x)) \wedge htree(l(x)) \cap htree(r(x)) = \emptyset\big) \qquad (1) \\ htree(x) ::=_{lfp}\ & ite\,(x = nil,\ \emptyset,\ htree\,(l(x)) \cup htree\,(r(x)) \cup \{x\}) \end{aligned}$$

In the above, *htree* maps each location x in the heap to the set of all locations reachable from x using l and r pointers, and the definition of *tree* uses this to ensure that the left and right trees are disjoint from each other and the root. Definitions in separation logic are similar (with heaplets being implicitly defined, and disjointness expressed using the separating conjunction '$\star$' [49, 50, 57]).

When performing imperative program verification, we annotate programs with loop invariants and contracts for methods, and reduce verification to validation of Hoare triples of the form $\{\alpha\}s\{\beta\}$, where s is a straight-line program (potentially with calls to other methods encoded using their contracts). The validity of each Hoare triple is translated to a pure logical validity question, called the *verification condition* (VC). When α and β refer to data structure properties, the resulting VCs

Authors' addresses: Adithya Murali, adithya5@illinois.edu, University of Illinois Urbana-Champaign, Department of Computer Science, Urbana, IL, USA; Cody Rivera, codyjr3@illinois.edu, University of Illinois Urbana-Champaign, Department of Computer Science, Urbana, Illinois, USA; P. Madhusudan, madhu@illinois.edu, University of Illinois Urbana-Champaign, Department of Computer Science, Urbana, Illinois, USA.

ACM XXXX-XXXX/2024/3-ART
https://doi.org/10.1145/nnnnnnn.nnnnnnn

are typically proved using induction on the structure of the recursive definitions. Automation of program verification reduces to automating validity of the logic the VCs are expressed in.

Logics that are powerful enough to express rich properties of data structures are invariably incomplete, not just undecidable, i.e., they do not admit any automated procedure that is complete (guaranteed to eventually prove any valid theorem, but need not terminate on invalid theorems). For instance, validity is incomplete for both first-order logic with least fixpoints and separation logic. Consequently, though verification frameworks like Dafny [35] support rich specification languages, validation of verification conditions can fail even for valid Hoare triples. Automated verification engines hence support several heuristics resulting in sound but incomplete verification.

When proofs succeed in such systems, the verification engineer is happy that automation has taken the proof through. However, when proofs *fail*, as they often do, the verification engineer is stuck and perplexed. First, they would crosscheck to see whether their annotations are strong enough and that the Hoare triples are indeed valid. If they believe they are, they do not have clear guidelines to help the tool overcome its incompleteness. Engineers are instead required to know the *underlying proof mechanisms/heuristics* the verification system uses in order to figure out why the system is unable to succeed, and figure out how to help the system. For instance, for data structures with recursive definitions, the proof system may just unfold definitions a few times, and the engineer must be able to see why this heuristic will not be able to prove the theorem and formulate new inductively provable lemmas or quantification triggers that can help. Such *unpredictable* verification systems that require engineers to know their internal heuristics and proof mechanisms frustrate verification experience.

Predictable Verification. In this paper, we seek an entirely new paradigm of *predictable* verification. We want a technique where:

(a) the verification engineer is asked to provide upfront a set of annotations that help prove programs correct, where these annotations are entirely *independent* of the verification mechanisms/tools, and

(b) the program verification problem, given these annotations, is guaranteed to be *decidable* (and preferably decidable using efficient engines such as SMT solvers).

The upfront agreement on the information that the verification engineer is required to provide makes their task crystal clear. The fact that the verification is decidable given these annotations ensures that the verification engine, given enough resources of time and space (of course) will eventually return proving the program correct or showing that the program or annotations are incorrect. There is no second-guessing by the engineer as the verification will never fail on valid theorems, and hence they need not worry about knowing how the verification engine works, or give further help. Note that the verification *without annotations* can (and typically will be) undecidable.

Intrinsic Definitions of Data Structures. In this paper, we propose an entirely new way of defining data structures, called *intrinsic definitions*, that facilitates a predictable verification paradigm for proving their maintenance. Rather than defining data structures using recursion, like in equation (1) above (which naturally calls for inductive proofs and invariably entails incompleteness), we define data structures by augmenting each location with additional information using *ghost maps* and demanding that certain *local conditions* hold between each location and its neighbors.

Intrinsic definitions formally require a set of monadic maps (maps of arity one) that associate values to each location in a structure (we can think of these as ghost fields associated with each location/object). We demand that the monadic maps on local neighborhoods of every location satisfy certain logical conditions. The existence of maps that satisfy the local logical conditions ensures that the structure is a valid data structure.

For example, we can capture trees in pointer-based heaps in the following way. Let us introduce maps $tree : Loc \rightarrow Bool$, $rank : Loc \rightarrow \mathbb{Q}^+$ (non-negative rationals), and $p : Loc \rightarrow Loc$ (for "parent"), and demand the following local property:

$$\begin{aligned}\forall x :: Loc.(tree(x) \Rightarrow (\ & (l(x) \neq nil \Rightarrow (tree(l(x)) \wedge p(l(x)) = x \wedge rank(l(x)) < rank(x))) \\ & \wedge (r(x) \neq nil \Rightarrow (tree(r(x)) \wedge p(r(x)) = x \wedge rank(r(x)) < rank(x))) \\ & \wedge ((l(x) \neq nil \wedge r(x) \neq nil) \Rightarrow l(x) \neq r(x)) \\ & \wedge (p(x) \neq nil \Rightarrow (r(p(x)) = x \vee l(p(x)) = x))\ )\end{aligned}$$

The above demands that ranks become smaller as one descends the tree, that a node is the parent of its children, and that a node is either the left or right child of its parent.

Given a *finite* heap, it is easy to see that if there exist maps *tree*, *rank* and p that satisfy the above property, and if $tree(l)$ is true for a location l, then l must point to a tree (strictly decreasing ranks ensure that there are no cycles and existence of a unique parent ensures that there are no "merges"). Furthermore, in any heap, if T is the subset of locations that are roots of trees, then there are maps that satisfy the above property and have precisely $tree(l)$ to be true for locations in T.

Note that the above intrinsic definition *does not use recursion* or least fixpoint semantics. It simply requires maps such that each location satisfies the local neighborhood condition.

Fix-what-you-break program verification methodology.

Intrinsic definitions are particularly attractive for proving *maintenance* of structures when structures undergo mutation. When a program mutates a heap H to a heap H', we start with monadic maps that satisfy local conditions in the pre-state. As the heap H is modified, we ask the verification engineer to also *repair* the monadic maps, using ghost map updates, so that the local conditions on all locations are met in the heap in the post-state H'.

For instance, consider a program that walks down a tree from its root to a node x and introduces a newly allocated node n between x and x's right child r. Then we would assume in the precondition that the monadic maps *tree*, *rank*, and p exist satisfying the local condition (2) above. After the mutation, we would simply update these maps so that $tree(n)$ is true, $p(r) = n$, $p(n) = x$, and $rank(n)$ is, say, $(rank(x) + rank(r))/2$.

The annotations required of the user, therefore, are ghost map updates to locations such that the local conditions are valid for each location. We will guarantee that checking whether the local conditions holds for each location, after the repairs, is expressible in decidable logics.

We propose a modular verification approach for verifying data structure maintenance that asks the programmer to fix what they break. Given a program that we want to verify, we instead verify an *augmented program* that keeps track of a ghost set of *broken locations Br*. Broken locations are those that (potentially) do not satisfy the local condition. When the program destructively modifies the fields of an object/location, it and some of its neighbors (accessible using pointers from the object) may not satisfy the local condition anymore, and hence will get added to the broken set. The verification engineer must repair the monadic maps on these broken locations and ensure (through an assertion) that the local condition holds on them before removing them from the broken set *Br*. However, even while repairing monadic maps on a location, the local condition on *its neighboring* locations may fail and get added to the broken set.

We develop a *fix-what-you-break (FWYB)* program verification paradigm, giving formal rules of how to augment programs with broken sets, how users can modify monadic maps, and fixed recipes of how broken sets are maintained in any program. In order to verify that a method m maintains a data structure, we need to prove that if m starts with the broken set being empty, it returns with the empty broken set. We prove this methodology sound, i.e., if the program augmented with broken

sets and ghost updates is correct, then the original program maintains the data structure properties mentioned in its contracts.

Decidable Verification of Annotated Programs. The general idea of using local conditions to capture global properties has been explored in the literature to reduce the complexity of proofs (e.g., iterated separation in separation logic [58]; see Section 6). Intrinsic definitions of data structures and the fix-what-you-break program verification methodology are more specifically designed to ensure the key property of *decidable verification of annotated programs* by avoiding both recursion/least-fixpoint definitions and avoiding even quantified reasoning.

The verification conditions for Hoare triples involving basic blocks of our annotated programs have the following structure. First, the precondition can be captured using *uninterpreted monadic functions* that are *implicitly* assumed to satisfy the local condition on each location that is not in the broken set Br (avoiding universal quantification). The monadic map updates (repairs) that the verification engineer makes can be captured using map updates. The postcondition of the ghost-code augmented program can, in addition to properties of variables, assert properties of the broken set Br using logics over sets. Finally, we show that capturing the modified heap after function calls can be captured using *parameterized map update* theories, that are decidable [19]. Consequently, the entire verification condition is captured in quantifier-free logics involving maps, parametric map updates, and sets over combined theories. These verification conditions are hence decidable and efficiently handled by modern SMT solvers[1].

Intrinsic Definitions for Representative Data Structures and Verification in Boogie. Intrinsic definitions of data structures is a novel paradigm and capturing data structures requires thinking anew in order to formulate monadic maps and local conditions that characterize them.

We give intrinsic definitions for several classic data structures such as linked lists, sorted lists, circular lists, trees, binary search trees, AVL trees, and red-black trees. These require novel definitions of monadic maps and local conditions. We also show how standard methods on these data structures (insertions, deletions, concatenations, rotations, balancing, etc.) can be verified using the fix-what-you-break strategy and standard loop invariant/contract annotations. We also consider *overlaid data structures* consisting of multiple data structures overlapping and sharing locations. In particular, we model the core of an overlaid data structure that is used in an I/O scheduler in Linux that has a linked list (modeling a FIFO queue) overlaid on a binary search tree (for efficient search over a key field). Intrinsic definitions beautifully capture such structures by compositionally combining the instrinsic definitions for each structure and a local condition linking them together. We show methods to modify this structure are provable using fix-what-you-break verification.

We model the above data structures and the annotated methods in the low-level programming language Boogie. Boogie is an intermediate programming language with verification support that several high-level programming languages compile to for verification (e.g., C [16, 17], Dafny [35], Civl [28], Move [20]). These annotated programs do not use quantifiers or recursive definitions, and Boogie is able to verify them automatically using decidable verification in negligible time, without further user-help.

Contributions. The paper makes the following contributions:

- A new paradigm of *predictable verification* that asks upfront for programmatic annotations and ensures annotated program verification is decidable, without reliance on users to give heuristics and tactics.
- A novel notion of intrinsic definitions of data structures based on ghost monadic maps and local conditions.

[1]Assuming of course that the underlying quantifier-free theories are decidable; for example, integer multiplication in the program or in local conditions would make verification undecidable, of course.

- A predictable verification methodology for programs that manipulate data structures with intrinsic definitions following a fix-what-you-break (FWYB) methodology.
- Intrinsic definitions for several classic data structures, and fix-what-you-break annotations for programs that manipulate such structures, with realization of these programs and their verification using Boogie.

2 INTRINSIC DEFINITIONS OF DATA STRUCTURES: THE FRAMEWORK

In this section we present the first main contribution of our paper, the framework of intrinsically defined data structures. We first define the notion of a data structure in a pointer-based heap.

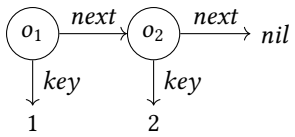
2.1 Data Structures

In this paper, we think of data structures defined using a *class* C of objects. The class C can coexist with other classes, heaps, and data structures, potentially modeled and reasoned with using other mechanisms. For technical exposition and simplicity, we restrict the technical definitions to a single class of data structures over a class C.

A class C has a signature $(\mathcal{S}, \mathcal{F})$ consisting of a finite set of sorts $\mathcal{S} = \{\sigma_0, \sigma_1 \ldots, \sigma_n\}$ and a finite set of fields $\mathcal{F} = \{f_1, f_2 \ldots, f_m\}$. We assume without loss of generality that the sort σ_0 represents the sort of objects of the class C, and we denote this sort by C itself. We use C to model objects in the heap. The other "background" sorts, e.g., integers, are used to model the values of the objects' fields. Each field $f_i : C \rightarrow \sigma$ is a unary function symbol and is used to model pointer and data fields of heap locations/objects. We model *nil* as a non-object value and denote the sort $C \uplus \{nil\}$ consisting of objects as well as the *nil* value by $C?$.

A C-heap H is a *finite* first-order model of the signature of C. More formally, it is a pair (O, I) where O is a finite set of *objects* interpreting the foreground sort C and I is an interpretation of every field in $\mathcal{F}$ for every object in O.

Example 2.1 (C-Heap). Let C be the class consisting of a pointer field $next : C \rightarrow C?$ and a data field $key : C \rightarrow Int$. The figure on the right represents a C-heap consisting of objects $O = \{o_1, o_2\}$ and the illustrated interpretation I for *next* and *key*. □



We now define a data structure. We fix a class C.

Definition 2.2 (Data Structure). A data structure D of arity k is a set of triples of the form $(O, I, \overline{o})$ such that (O, I) is a C-heap and $\overline{o}$ is a k-tuple of objects from O. □

Informally, a data structure is a particular subset of C-heaps along with a distinguished tuple of locations $\overline{o}$ in the heap that serve as the "entry points" into the data structure, such as the root of a tree or the ends of a linked list segment.

Example 2.3 (Sorted Linked List). Let C be the class defined in Example 2.1. The data structure of sorted linked lists is the set of all (O, I, o_1) such that O contains objects $o_1, o_2 \ldots o_n$ with the interpretation $next(o_i) = o_{i+1}$ and $key(o_i) \leq key(o_{i+1})$ for every $1 \leq i < n$, and $next(o_n) = nil$. For example, let (O, I) be the C-heap described in Example 2.1. The triple (O, I, o_1) is an example of a sorted linked list. Here o_1 represents the head of the sorted linked list. □

2.2 Intrinsic Definitions of Data Structures

In this work, we propose a characterization of data structures using *intrinsic definitions*. Intrinsic definitions consist of a set of *monadic maps* that associate (ghost) values to each object and a set of *local* conditions that constrain the monadic maps on each location and its neighbors. A C-heap is

considered to be a valid data structure if *there exists* a set of monadic maps for the heap that satisfy the local conditions.

Annotations using intrinsic definitions enable local and decidable reasoning for correctness of programs manipulating data structures using the Fix-What-You-Break (FWYB) methodology, which is described later in Section 3. We develop the core idea of intrinsic definitions below.

Ghost Monadic Maps. We denote by $C_{\mathcal{G}} = (\mathcal{S}, \mathcal{F} \cup \mathcal{G})$ an extension of C with a finite set of monadic (i.e., unary) function symbols $\mathcal{G}$. We can think of these as *ghost* fields of objects.

The key idea behind intrinsic definitions is to extend a C-heap with a set of ghost monadic maps and formulate local conditions using the maps that characterize the heaps belonging to the data structure. The existence of such ghost maps satisfying the local conditions is then the intrinsic definition. Definitions are parameterized by a multi-sorted first-order logic $\mathcal{L}$ in which local conditions are stated. The logic has the sorts $\mathcal{S}$ and contains the function symbols in $\mathcal{F} \cup \mathcal{G}$, as well as interpreted functions over background sorts (such as + and < on integers, and $\subseteq$ on sets).

Definition 2.4 (Intrinsic Definition). Let $C = (\mathcal{S}, \mathcal{F})$ be a class. An intrinsic definition $IDS(\overline{y})$ over the class C is a tuple $(\mathcal{G}, \mathcal{L}, LC, \varphi(\overline{y}))$ where:

(1) $\mathcal{G}$ is a finite set of *monadic map names and function signatures* disjoint from $\mathcal{F}$,
(2) $\mathcal{L}$ is a first-order logic over the sorts $\mathcal{S}$ containing the interpreted functions of the background sorts as well as the function symbols in $\mathcal{F} \cup \mathcal{G}$,
(3) A *local condition* formula LC of the form $\forall x : Loc.\, \rho(x)$ such that ρ is a quantifier-free $\mathcal{L}$-formula, and
(4) A *correlation formula* $\varphi(\overline{y})$ that is a quantifier-free $\mathcal{L}$-formula over free variables $\overline{y} \in Loc$. □

We denote an intrinsic definition by $(\mathcal{G}, LC, \varphi(\overline{y}))$ when the logic $\mathcal{L}$ is clear from context. In this work $\mathcal{L}$ is typically a decidable combination of quantifier-free theories [46, 47, 62], containing theories of integers, sets, arrays [19], etc., supported effectively in practice by SMT solvers [8, 18].

Definition 2.5 (Data Structures defined by Intrinsic Definitions). Let $C = (\mathcal{S}, \mathcal{F})$ be a class and $IDS(\overline{y}) = (\mathcal{G}, LC, \varphi(\overline{y}))$ be an intrinsic definition over C consisting of monadic maps $\mathcal{G}$, local condition LC and correlation formula φ. The data structure defined by IDS is precisely the set of all $(O, I, \overline{o})$ where *there exists* an interpretation J that extends I with interpretations for the symbols in $\mathcal{G}$ such that $O, J \models LC$ and $O, J[\overline{y} \mapsto \overline{o}] \models \varphi(\overline{y})$, where $[\overline{y} \mapsto \overline{o}]$ denotes that the free variables $\overline{y}$ are interpreted as $\overline{o}$.

Informally, given a data structure DS consisting of triples $(O, I, \overline{o})$, an intrinsic definition demands that there exist monadic maps $\mathcal{G}$ such that the C-heaps (O, I) in the data structure can be extended with values for maps in $\mathcal{G}$ satisfying the local conditions LC, and the entrypoints $\overline{o}$ are characterized in the extension by the quantifier-free formula φ.

Example 2.6 (Sorted Linked List). Recall the data structure of sorted linked lists defined in Example 2.3. We capture sorted linked lists by an intrinsic definition $SortedLL(y)$ using monadic maps $sortedll : C \to Bool$ and $rank : C \to \mathbb{Q}^+$ such that:

$$LC \equiv \forall x.\, \Big((sortedll(x) \wedge next(x) \neq nil) \Rightarrow (sortedll(next(x)) \wedge rank(next(x)) < rank(x) \wedge key(x) \leq key(next(x))) \Big)$$
$$\varphi(y) \equiv sortedll(y)$$

In the above definition the *rank* field decreases wherever *sortedll* holds as we take the *next* pointer, and hence assures that there are no cycles. Observe that without the constraint on *rank*, the triple $(\{o_1, o_2\}, I, o_1)$ where $I = \{next(o_1) = o_2, next(o_2) = o_1, key(o_1) = key(o_2) = 0\}$ denoting a two-element circular list would satisfy the definition, which is undesirable.

$$
\begin{aligned}
P :=\ & x := nil \mid x := y \mid v := be \mid y := x.f \mid v := x.d \\
& \mid x.f := y \mid x.d := v \mid x := \text{new } C() \mid \overline{r} := Function(\overline{t}) \\
& \mid \text{skip} \mid \text{assume } cond \mid \text{return} \mid P\,;P \mid \text{if } cond \text{ then } P \text{ else } P \mid \text{while cond do } P \\
cond :=\ & x = y \mid x \neq y \mid be \quad \text{(Condition Expressions)}
\end{aligned}
$$

Fig. 1. Grammar of while programs with recursion. x, y are variables denoting objects of class C? (i.e., C objects or *nil*), v, w are a background sort(s) variables, r, t denote variables of any sort, f is a pointer field, d is a data field, and be is a expression of the background sort(s).

Note that the above allows for a heap to contain both sorted lists as well as unsorted lists. We are guaranteed by the local condition that the set of all objects where *sortedll* is true will be the heads of sorted lists.

We can also replace the domain of ranks in the above definition using any strict partial order, say integers or reals (with the usual $<$ order on them), and the definition will continue to define sorted lists. Well-foundedness of the order is not important as heaps are *finite* in our work (see definition of C-heaps in Section 2.1) □

3 FIX WHAT YOU BREAK (FWYB) VERIFICATION METHODOLOGY

In this section we present the second main contribution of this paper: the Fix-What-You-Break (FWYB) methodology. We begin by describing a while programming language and defining the verification problem we study. We fix a class $C = (\mathcal{S}, \mathcal{F})$ throughout this section.

3.1 Programs, Contracts, and Correctness

Programs. Figure 1 shows the programming language used in this work. Note that we can use variables and expressions over non-object sorts. Functions can return multiple outputs. We assume that method signatures contain designated output variables and therefore the return statement does not mention values.

Our language is safe (i.e., allocated locations cannot point to un-allocated locations) and garbage-collected. Formally we consider configurations θ consisting of a store (map from variables to values) and a heap along with an error state $\perp$ to model error on a null dereference. We denote that a formula α is satisfied on a configuration θ by writing $\theta \models \alpha$.

Intrinsic Hoare Triples. The verification problem we study in this paper is *maintenance* of data structure properties. Fix an intrinsic definition $(\mathcal{G}, LC, \varphi(\overline{y}))$ where $\mathcal{G} = \{g_1, g_2 \ldots, g_k\}$. Let $\overline{z}$ be the input/output variables for a program that we want to verify. We consider pre and post conditions of the form

$$\exists\, g_1, g_2 \ldots, g_k.\, (LC \wedge \varphi(\overline{w}) \wedge \psi(\overline{z}))$$

where each g_i is a ghost monadic map (unary function over locations), ψ is a quantifier-free formula over $\overline{z}$ that can use the ghost monadic maps g_i, and $\overline{w}$ is a tuple of variables from $\overline{z}$ whose arity is equal to $\overline{y}$. Note that the above has a second-order existential quantification ($\exists$) over function symbols $g_1, \ldots, g_k$, and LC has first-order universal quantification over a single location variable. Read in plain English, "$\overline{w}$ points to a data structure *IDS* such that the (quantifier-free) property $\psi(\overline{z})$ holds".

We study the validity of the following Hoare Triples:

$$\langle\, \alpha(\overline{x}) \,\rangle \;\; \mathrm{P}(\overline{x},\ ret\colon\ \overline{r}) \;\; \langle\, \beta(\overline{x}, \overline{r}) \,\rangle$$

where α and β are pre and post conditions of the above form, P is a program, and $\overline{x}, \overline{r}$ are input and output variables for P respectively.

Example 3.1 (Running Example: Insertion into a Sorted List). Let $SortedLL(y) = (\mathcal{G}, LC, sorted(y))$ as in Example 2.6 where $\mathcal{G} = \{sortedll, rank\}$. The following Hoare triple says that insertion into a sorted list returns a sorted list:

$\langle\, \exists\, sortedll, rank.\, LC \wedge sortedll(x) \,\rangle\ sorted\text{–}insert(x, k,\ ret\colon x)\ \langle\, \exists\, sortedll, rank.\, LC \wedge sortedll(x) \,\rangle$

where x, r are variables of type C, k is of type *Int* and *sorted–insert* is the usual recursive method.

Validity of Intrinsic Hoare Triples. We now define the validity of Hoare Triples.

Definition 3.2 (Validity of Intrinsic Hoare Triples). An intrinsic triple $\langle\, \alpha \,\rangle\, P\, \langle\, \beta \,\rangle$ is *valid* if for every configuration θ such that $\theta \models \alpha$, transitioning according to P starting from θ does not encounter the error state $\perp$, and furthermore, if θ transitions to θ' under P, then $\theta' \models \beta$.

3.2 Ghost Code

In this work we consider the augmentation of procedures with *ghost* or non-executed code. Ghost code involves the manipulation of a set of distinct *ghost variables* and *ghost fields*, distinguished from regular or 'user' variables and fields. In program verification, ghost code provides a programmatic way of constructing values/functions that witness a particular property.

We defer a formal definition of ghost code to the Appendix of our technical report [1] and only provide intuition here. Intuitively, ghost variables/fields cannot influence the computation of non-ghost variables/fields. Therefore, ghost variables and maps can be assigned values from user variables and maps, but the reverse is not allowed. Similarly, when conditional statements or loops use ghost variables in the condition, the body of the statement must also consist entirely of ghost code. Simply, ghost code cannot control the flow of the user program. These conditions can be checked statically. Finally, we also require that ghost loops and functions always terminate since nonterminating ghost code can change the meaning of the original program. Our definition is agnostic to the technique used to establish termination, however, we use ranking functions to establish termination in our implementation in Dafny.

We formalize the above into a grammar that extends the original programming language in Figure 1 into a ghost code-augmented language in Figure 6 in Appendix A of our technical report [1]. The language of ghost programs is similar to P in Figure 1, except that we do not have allocation or assume statements, and loops/functions must always terminate. See prior literature for a more detailed formal treatment of ghost code [24, 27, 38, 56].

Projection that Eliminates Ghost Code. We can define the notion of 'projecting out' ghost code, which takes a program that contains ghost code and yields a pure user program with all ghost code simply eliminated. Intuitively, the fact that ghost code does not affect the execution of the underlying user program makes the projection operation sensible.

Fix a main method M with body P. Let $N_i, 1 \leq i \leq k$ be a set of auxiliary methods with bodies Q_i that P can call. Note that the bodies P and Q_i contain ghost code. Let us denote a program containing these methods by $[(M : P); (N_1 : Q_1) \ldots (N_k : Q_k)]$. We then define projection as follows:

Definition 3.3 (Projection of Ghost-Augmented Code to User Code). The projection of the ghost-augmented program $[(M : P); (N_1 : Q_1) \ldots (N_k : Q_k)]$ is the user program $[(\hat{M} : \hat{P}); (\hat{N_1} : \hat{Q_1}) \ldots (\hat{N_k} : \hat{Q_k})]$ such that:

(1) The input (resp. output) signature of $\hat{M}$ is that of M with the ghost input (resp. output) parameters removed.
(2) $\hat{P}$ is derived from P by: (a) eliminating all ghost code, and (b) replacing each non-ghost function call statement of the form $\overline{r} := N_j(\overline{t})$ with the statement $\overline{s} := \hat{N_j}(\overline{u})$, where $\overline{u}$ is the

non-contiguous subsequence of $\bar{t}$ with the elements corresponding to ghost input parameters removed and $\bar{s}$ is obtained from $\bar{r}$ similarly. Each $\hat{Q}_i$ is derived from the corresponding Q_i by a similar transformation.

We provide an expanded version of this definition in our technical report [1] in Appendix A.

An Overview of FWYB

We develop the Fix-What-You-Break (FWYB) methodology in three stages, in the following subsections. We give here an overview of the methodology and the stages.

Recall that intrinsic triples are of the form $\langle\, \exists\, g_1, g_2 \ldots, g_k.\, (LC \wedge \varphi \wedge \alpha)\, \rangle\ P\ \langle\, \exists\, g_1, g_2 \ldots, g_k.\, (LC \wedge \varphi \wedge \beta)\, \rangle$. In Stage 1 (Section 3.3) we remove the second-order quantification. We do this by requiring the verification engineer to explicitly construct the g_i maps in the post state from the maps in the pre state using *ghost code*. We then obtain triples of the form $\langle\, LC \wedge \varphi \wedge \alpha\, \rangle\ P_{\mathcal{G}}\ \langle\, LC \wedge \varphi \wedge \beta\, \rangle$ where $P_{\mathcal{G}}$ is an augmentation of P with ghost code that updates the $\mathcal{G}$ maps.

Note that the LC in the contract universally quantifies over objects. In Stages 2 (Section 3.4) and 3 (Section 3.5) we remove the quantification by explicitly tracking the objects where the local conditions do not hold and treating them as implicitly true on all other objects. We call this set Br the *broken set*. Intuitively, the broken set grows when the program mutates pointers or makes other changes to the heap, and shrinks when the verification engineer repairs the $\mathcal{G}$ maps using ghost code to satisfy the LC on the broken objects. The specifications assume an empty broken set at the beginning of the program and the engineer must ensure that it is empty again at the end of the program. However, they do not have to track the objects manually. We develop in Stage 3 (Section 3.5) a discipline for writing only *well-behaved* manipulations of the broken set. This reduces the problem to triples of the form $\langle\, \varphi \wedge \alpha\, \rangle\ P_{\mathcal{G},Br}\ \langle\, \varphi \wedge \beta\, \rangle$, where $P_{\mathcal{G},Br}$ contains ghost code for updating both $\mathcal{G}$ and Br. Note that these specifications are quantifier-free, and checking them can be effectively automated using SMT solvers [8, 18].

3.3 Stage 1: Removing Existential Quantification over Monadic Maps using Ghost Code

Consider an intrinsic Hoare Triple $\langle\, \exists\, g_1, g_2 \ldots, g_k.\, (LC \wedge \varphi \wedge \alpha)\, \rangle\ P\ \langle\, \exists\, g_1, g_2 \ldots, g_k.\, (LC \wedge \varphi \wedge \beta)\, \rangle$. Read simply, the precondition says that *there exist* maps $\{g_i\}$ satisfying some properties, and the postcondition says that we must *show the existence* of maps $\{g_i\}$ satisfying the post state properties.

We remove existential quantification from the problem by re-formulating it as follows: we assume that we are *given* the maps $\{g_i\}$ as part of the pre state such that they satisfy $LC \wedge \varphi \wedge \alpha$, and we require the verification engineer to *compute* the $\{g_i\}$ maps in the post state satisfying $LC \wedge \varphi \wedge \beta$. The engineer computes the post state maps by taking the given pre state maps and 'repairing' them on an object whenever the program breaks local conditions on that object. The repairs are done using ghost code, which is a common technique in verification literature [24, 27, 38, 56].

Formally, fix an intrinsically defined data structure $(\mathcal{G}, LC, \varphi)$. We extend the class signature $C = (\mathcal{S}, \mathcal{F})$ (and consequently the programming language) to $C_{\mathcal{G}} = (\mathcal{S}, \mathcal{F} \cup \mathcal{G})$ and treat the symbols in $\mathcal{G}$ as *ghost fields* of objects of class C in the program semantics. Performing the transformation described above reduces the verification problem to proving triples of the form $\langle\, LC \wedge \varphi \wedge \alpha\, \rangle\ P_{\mathcal{G}}\ \langle\, LC \wedge \varphi \wedge \beta\, \rangle$, where there is no existential quantification over $\mathcal{G}$ and $P_{\mathcal{G}}$ is an augmentation of P with ghost code that updates the $\mathcal{G}$ maps. The following proposition captures the correctness of this reduction:

Proposition 3.4. *Let ψ_{pre} and ψ_{post} be quantifier-free formulae over $\mathcal{F} \cup \mathcal{G}$. If $\langle\, LC \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G}}\ \langle\, LC \wedge \psi_{post} \,\rangle$ is valid then $\langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre} \,\rangle\ P\ \langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post} \,\rangle$ is valid [2], where P is the projection of $P_{\mathcal{G}}$ obtained by eliminating ghost code.*

Proof Gist. We provide the full proof in our technical report [1] and furnish a gist here. First, observe that the semantics of validity for the two triples are over different configuration spaces: one with interpretations for ghost variables and maps, and one without. Given a configuration C that gives interpretation to ghost variables/maps, define $\hat{C}$ to be the *projection* that eliminates the ghost interpretations. Conversely, we refer to C as an *extension* of $\hat{C}$. We define $\hat{\bot} = \bot$.

The key observation is that if a ghost code augmented procedure M starting from a configuration C_1 reaches C_2, then its projection $\hat{M}$ starting from $\hat{C_1}$ reaches $\hat{C_2}$. Informally, this means that projection preserves the validity of Hoare Triples. We show this lemma in Appendix B of our technical report [1]. Since $\langle\, LC \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G}}\ \langle\, LC \wedge \psi_{post} \,\rangle$ is valid, we can use the rule of consequence to conclude that $\langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G}}\ \langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post} \,\rangle$ is valid.

Now, fix configurations (without ghost state) c_1, c_2 such that c_1 satisfies $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre}$ and P starting from c_1 reaches c_2. To show that the given Hoare triple for P is valid, we must establish that c_2 is not $\bot$, and further that c_2 satisfies $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$.

Since $c_1 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre}$, by the semantics of second order logic there exists a configuration (taken as a model) extending c_1, say C_1, such that $C_1 \models LC \wedge \psi_{pre}$. Since $\langle\, LC \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G}}\ \langle\, LC \wedge \psi_{post} \,\rangle$ is valid, we know that $P_{\mathcal{G}}$ starting from C_1 reaches some C_2 such that C_2 is not $\bot$ and $C_2 \models LC \wedge \psi_{post}$. We can weaken the postcondition and conclude that $C_2 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$

Finally, since c_1 is the projection of C_1 and P is the projection of $P_{\mathcal{G}}$ we have from the lemma described above that $\hat{C_2} = c_2$. Therefore, $c_2 \neq \bot$ since $C_2 \neq \bot$. Further, observe that $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$ does not refer to any un-quantified ghost fields or variables and is stated over the common vocabulary of c_2 and C_2. Since c_2 and C_2 agree on the interpretations of symbols in the common vocabulary (by definition of projection) and $C_2 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$, we have that $c_2 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$. This concludes the proof. □

We note a point of subtlety about the reduction in this stage here: the simplified triple eliminates existential quantification over $\mathcal{G}$ by claiming something stronger than the original specification, namely that for *any* maps $\{g_i\}$ such that ψ_{pre} is satisfied in the pre state, there is a *computation* that yields corresponding maps in the post state such that ψ_{post} holds. The onus of coming up with such a computation is placed on the verification engineer.

3.4 Stage 2: Relaxing Universal Quantification using Broken Sets

We turn to verifying programs whose pre and post conditions are of the form $LC \wedge \gamma$, where $LC \equiv \forall z.\, \rho(z)$ is the local condition. Consider a program P that maintains the data structure. The local conditions are satisfied everywhere in both the pre and post state of P. However, they need not hold everywhere in the intermediate states. In particular, P may call a method N which may neither receive nor return a proper data structure. To reason about P modularly we must be able to express contracts for methods like N. To do this we must be able to talk about program states where only some objects may satisfy the local conditions.

Broken Sets. We introduce in programs a ghost set variable Br that represents the set of (potentially) broken objects. Intuitively, at any point in the program the local conditions must always be satisfied on every object that is *not* in the broken set. Formally, for a program P we extend the signature of P with Br as an additional input and an additional output. We also write pre and post conditions of

[2]Here the notion validity for both triples is given by Definition 3.2, where configurations are interpreted appropriately with or without the ghost fields.

the form $(\forall z \notin Br.\, \rho(z)) \wedge \gamma$ to denote that local conditions are satisfied everywhere outside the broken set, where γ can now use Br. In particular, given the Hoare triple

$$\langle\, (\forall z.\, \rho(z)) \wedge \alpha \,\rangle\ P_{\mathcal{G}}(\overline{x},\ ret\colon\ \overline{y})\ \langle\, (\forall z.\, \rho(z)) \wedge \beta \,\rangle$$

from Stage 1, we instead prove the following Hoare triple (whose validity implies the validity of the triple above):

$$\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha \wedge Br = \emptyset \,\rangle\ P_{\mathcal{G},Br}(\overline{x}, Br,\ ret\colon\ \overline{y}, Br)\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta \wedge Br = \emptyset \,\rangle$$

where Br is a ghost input variable of the type of set of objects and $P_{\mathcal{G},Br}$ is an augmentation of P with ghost code that computes the $\mathcal{G}$ maps as well as the Br set satisfying the postcondition.

P may also call other methods N with bodies Q. We similarly extend the input and output signatures of the called methods and use the broken set to write appropriate contracts for the methods, introducing triples of the form $\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha_N \,\rangle\ Q_{Br}(\overline{s}, Br,\ ret\colon\ \overline{r}, Br)\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta_N \,\rangle$. Again, $Q_{\mathcal{G},Br}$ is an augmentation of Q with ghost code that updates $\mathcal{G}$ and Br.

For the main method that preserves the data structure property, the broken set is empty at the beginning and end of the program. However, called methods or loop invariants can talk about states with nonempty broken sets. We require the verification engineer to write ghost code that maintains the broken set accurately. The soundness of this reduction is captured by the following Proposition:

Proposition 3.5. *Let α and β be quantifier-free formulae over $\mathcal{F} \cup \mathcal{G}$ (they cannot mention Br). If $\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha \wedge Br = \emptyset \,\rangle\ P_{\mathcal{G},Br}(\overline{x}, Br,\ ret\colon\ \overline{y}, Br)\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta \wedge Br = \emptyset \,\rangle$ is valid then $\langle\, (\forall z.\, \rho(z)) \wedge \alpha \,\rangle\ P_{\mathcal{G}}(\overline{x},\ ret\colon\ \overline{y})\ \langle\, (\forall z.\, \rho(z)) \wedge \beta \,\rangle$ is valid, where $P_{\mathcal{G}}$ is the projection of $P_{\mathcal{G},Br}$ obtained by eliminating the statements that manipulate Br.*

The proof of this proposition is similar to the proof of Proposition 3.4, except that projections only eliminate Br. We provide a detailed argument in our technical report [1] in Appendix B.

3.5 Stage 3: Eliminating the Universal Quantifier for Well-Behaved Programs

We consider triples of the form

$$\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha \,\rangle\ P_{\mathcal{G},Br}(\overline{x}, Br,\ ret\colon\ \overline{y}, Br)\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta \,\rangle$$

where $P_{\mathcal{G},Br}$ is a program augmented with ghost updates to the $\mathcal{G}$-fields as well as the Br set, and α, β are quantifier-free formulae that can also mention the fields in $\mathcal{G}$ and the Br set. In this stage we would like to eliminate the quantified conjunct entirely and instead ask the engineer to prove the validity of the triple

$$\{\alpha\}\ P_{\mathcal{G},Br}(\overline{x}, Br,\ ret\colon\ \overline{y}, Br)\ \{\beta\}$$

However, the above two triples are not, in general, equivalent (as broken sets can be manipulated wildly). In this section we define a syntactic class of *well-behaved* programs that force the verification engineer to maintain broken sets correctly, and for such programs the above triple are indeed equivalent. For example, for a field mutation, well-behaved programs require the engineer to determine the set of *impacted objects* where local conditions may be broken by the mutation. The well-behavedness paradigm then mandates that the engineer add the set of impacted objects to the broken set immediately following the mutation statement. Similarly, well-behaved programs do not allow the engineer to remove an object from the broken set unless they show that the local conditions hold on that object. The imposition of this discipline ensures that programmers carefully preserve the meaning of the broken set (i.e., objects outside the broken set must satisfy local conditions). This allows for the quantified conjunct in the triple obtained from Stage 2 to be dropped since it always holds for a well-behaved program. Let us look at such a program:

Example 3.6 (Well-Behaved Sorted List Insertion). We use the running example (Example 3.1) of insertion into a sorted list. We consider a snippet where the key k to be inserted lies between the keys of x and $next(x)$ (which we assume is not nil). We ignore the conditionals that determine $next(x) \neq nil$ and $key(x) \leq k \leq key(next(x))$ for brevity.

We first relax the universal quantification as described in Stage 2 (Section 3.4) and rewrite the pre and post conditions to $(\forall\, z \notin Br.\, LC(z)) \wedge sortedll(x) \wedge Br = \emptyset$. Making the first conjunct implicit, we write the following program that manipulates the broken set in a well-behaved manner. We show the value of the broken set through the program in comments on the right:

```
pre: sortedll(x) ∧ Br = ∅
post: sortedll(x) ∧ Br = ∅
assert x ∉ Br;
assume LC(x);
y := x.next;     // {}
z := new C();
Br := Br ∪ {z}; // {z}
z.key := k;
Br := Br ∪ {z}; // {z}
z.next := y;
Br := Br ∪ {z}; // {z}
z.sortedll := True;
Br := Br ∪ {z}; // {z}
x.next := z;
Br := Br ∪ {x}; // {x,z}
z.rank := (x.rank + y.rank)/2;
Br := Br ∪ {z}; // {x,z}
// x and z satisfy LC
assert LC(z);
Br := Br \ {z}; // {x}
assert LC(x);
Br := Br \ {x}; // {}
```

We depict the statements enforced by the well-behavedness paradigm in pink and the ghost updates written by the verification engineer in blue. Observe that the paradigm adds the impacted objects to the broken set after each mutation and allocation. Determining the impact set of a mutation is nontrivial; we show how to construct them in Section 4.1. Note also that to remove x from the broken set we must show $LC(x)$ holds (assert followed by removal from Br). Finally, we see at the beginning of the snippet that if we show $x \notin Br$ then we can infer that $LC(x)$ holds. This follows from the meaning of the broken set.

Putting it All Together. The above program corresponds to the program $P_{\mathcal{G},Br}$ obtained from the Stage 3 reduction, consisting of ghost updates to the $\mathcal{G}$ maps and Br. Since it is well-behaved and satisfies the contract $\langle\, sortedll(x) \wedge Br = \emptyset \,\rangle\ P_{\mathcal{G},Br}\ \langle\, sortedll(x) \wedge Br = \emptyset \,\rangle$ we can conclude that it satisfies the contract $\langle\, (\forall z \notin Br.\, \rho(z)) \wedge sortedll(x) \wedge Br = \emptyset \,\rangle\ P_{\mathcal{G},Br}\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge sortedll(x) \wedge Br = \emptyset \,\rangle$. Using Propositions 3.4 and 3.5 we can project out all augmented code and conclude that the triple given in Example 3.1 with the user's original program and intrinsic specifications is valid! In this way, using FWYB we can verify programs with respect to intrinsic specifications by verifying augmented programs with respect to quantifier-free specifications. The latter can be discharged efficiently in practice using SMT solvers [8, 18] (see Section 3.7). □

We dedicate the rest of this section to developing the general theory of well-behaved programs.

Rules for Constructing Well-Behaved Programs

We define the class of well-behaved programs using a set of rules. We first introduce some notation.

We distinguish the triples over the augmented programs and quantifier-free annotations by $\{\psi_{pre}\}\ P\ \{\psi_{post}\}$, with $\{\}$ brackets rather than $\langle\,\rangle$. We denote that a triple is provable by $\vdash \{\psi_{pre}\}\ P\ \{\psi_{post}\}$. Our theory is agnostic to the underlying mechanism for proving triples correct (we use the off-the-shelf verification tool Boogie in our evaluation). However, we assume that the mechanism is sound with respect to the operational semantics. We denote that a snippet P is well-behaved by $\vdash_{\text{WB}} P$. We also denote that local conditions hold on an object x by $LC(x)$.

Figure 2 shows the rules for writing well-behaved programs. We only explain the interesting cases here.

Mutation. Since mutations can break local conditions, we must grow the broken set. Let A be a finite set of object-type terms over x such that for any $z \notin A$, if $LC(z)$ held before the mutation, then it continues to hold after the mutation. We refer to such a set A as an *impact set* for the mutation, and we update Br after a mutation with its impact set. The impact set may not always be expressible as a finite set of terms, but this is indeed the case for all the intrinsically defined data structures we use in this paper. We show how to construct impact sets in Section 4.1.

Allocation. Allocation does not modify the heap on any existing object. Therefore, we simply update the broken set by adding the newly created object x (this was also the case in Example 3.6).

Assert LC and Remove. This rule allows us to shrink the broken set once the verification engineer fixes the local conditions on a broken location. The snippet `assert LC(x); Br := Br\{x}` in Example 3.6 uses this rule. Informally, the verification engineer is required to show that $LC(x)$ holds before removing x from Br.

Infer LC Outside Br. Recall that for well-behaved programs we know implicitly that $\forall x \notin Br.\ \rho(x)$ holds. This rule allows us to instantiate this implicit fact on objects that we can show lie outside the broken set. The snippet `assert x∉Br; assume LC(x)` in example 3.6 uses this rule.

We show that the above rules are sound for the elimination of the universal quantifier in Stage 3:

Proposition 3.7. *Let* $[(M : P); (N_1 : Q_1) \ldots, (N_k : Q_k)]$ *be a program (which can use* $\mathcal{G}$ *and* Br*) such that* $\vdash_{\text{WB}} P$ *and* $\vdash_{\text{WB}} Q_i, 1 \leq i \leq k$. *Let* α *and* β *be quantifier-free formulae over* $\mathcal{F} \cup \mathcal{G}$ *which can use* Br. *If* $\{\alpha\}\ P(\overline{x}, Br,\ ret : \overline{y}, Br)\ \{\beta\}$ *is valid, then* $\langle\ (\forall z \notin Br.\ \rho(z)) \wedge \alpha\ \rangle\ P(\overline{x}, Br,\ ret : \overline{y}, Br)\ \langle\ (\forall z \notin Br.\ \rho(z)) \wedge \beta\ \rangle$ *is valid.*

We prove the above proposition by structural induction on the rules in Figure 2. We provide the proof in Appendix B of our technical report [1].

In the above presentation we use only one broken set for simplicity of exposition. Our general framework allows for finer-grained broken sets that can track breaks over a partition on the local conditions. For example, in Section 4.4 we verify deletion in an overlaid data structure consisting of a linked list and a binary search tree using two broken sets: one each for the local conditions of the two component data structures.

3.6 Soundness of FWYB

In this section we state the soundness of the FWYB methodology.

Theorem 3.8 (FWYB Soundness). *Let* $(\mathcal{G}, LC, \varphi)$ *be an intrinsic definition with* $\mathcal{G} = \{g_1, g_2 \ldots, g_l\}$. *Let* $[(M : P); (N_1 : Q_1) \ldots, (N_k : Q_k)]$ *be an augmented program constructed using the FWYB methodology such that* $\vdash_{\text{WB}} P$ *and* $\vdash_{\text{WB}} Q_i$, $1 \leq i \leq k$, *i.e., the programs* P *and* Q_i *are well-behaved (according to the rules in Figure 2). Let* φ, ψ_{pre}, *and* ψ_{post} *be quantifier-free formulae that do not mention* Br *(but can mention the maps in* $\mathcal{G}$*). Finally, let* $[(\hat{M} : \hat{P}); (\hat{N}_1 : \hat{Q}_1) \ldots, (\hat{N}_k : \hat{Q}_k)]$ *be the projected user-level program according to Definition 3.3. Then, if the triple:*

$$\{\varphi \wedge \psi_{pre} \wedge Br = \emptyset\}\ P\ \{\varphi \wedge \psi_{post} \wedge Br = \emptyset\}$$

is valid, then the triple

$$\langle\ \exists\, g_1, g_2 \ldots, g_l.\ (LC \wedge \varphi \wedge \psi_{pre})\ \rangle\ \hat{P}\ \langle\ \exists\, g_1, g_2 \ldots, g_l.\ (LC \wedge \varphi \wedge \psi_{post})\ \rangle$$

is valid (according to Definition 3.2).

Informally, the soundness theorem says that given a user-written program, if we (a) augment it with updates to ghost fields and the broken set only using the discipline for well-behaved programs, and (b) show that if the broken set is empty at the beginning of the program it will be empty at the end, then the original user-written program satisfies the intrinsic specifications on preservation of the data structure.

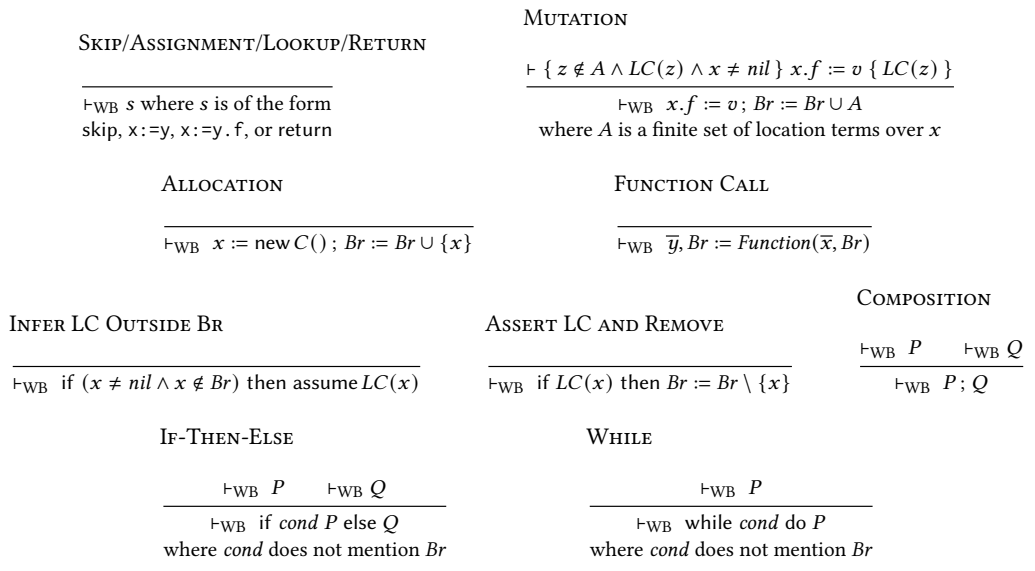


Fig. 2. Rules for constructing well-behaved programs. Local condition formula instantiated at x is denoted by $LC(x)$. The statement (if *cond* then S) is sugar for (if *cond* then S else skip).

The proof of the theorem trivially follows from the soundness of the three stages. Let us write P as $P_{\mathcal{G},Br}$ to emphasize that the program contains ghost code that manipulates both the $\mathcal{G}$ maps and Br. We begin with the fact that $\{\varphi \wedge \psi_{pre} \wedge Br = \emptyset\}\ P_{\mathcal{G},Br}\ \{\varphi \wedge \psi_{post} \wedge Br = \emptyset\}$ is valid. Since P and its auxiliary functions are well-behaved we have from Proposition 3.7 that $\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \varphi \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G},Br}\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \varphi \wedge \psi_{post} \,\rangle$ is valid.

Next, we use Proposition 3.5 to conclude that $\langle\, (\forall z.\, \rho(z)) \wedge \varphi \wedge \psi_{pre} \,\rangle\ P_{\mathcal{G}}\ \langle\, (\forall z.\, \rho(z)) \wedge \varphi \wedge \psi_{post} \,\rangle$ is valid, where $P_{\mathcal{G}}$ is the projection of $P_{\mathcal{G},Br}$ obtained by eliminating the statements that manipulate Br. Finally, we use Proposition 3.4, along with the fact that $\forall z.\, \rho(z)$ is LC and $\hat{P_{\mathcal{G}}}$ is the same as $\hat{P}$ to conclude that $\langle\, \exists\, g_1, g_2 \ldots, g_l.\, (LC \wedge \varphi \wedge \psi_{pre}) \,\rangle\ \hat{P}\ \langle\, \exists\, g_1, g_2 \ldots, g_l.\, (LC \wedge \varphi \wedge \psi_{post}) \,\rangle$ is valid[3]. □

3.7 Generating Quantifier-Free Verification Conditions

We state at several points in this paper that verifying augmented programs with quantifier-free specifications reduces to validity over combinations of quantifier-free theories. However, this is not obvious. Unlike scalar programs, quantifier-free contracts do not guarantee quantifier-free verification conditions (VCs) for heap programs. In particular, commands such as allocation and function calls pose challenges. However, we show that in our case it is indeed possible to obtain quantifier-free VCs. We do this by transforming a given heap program into a scalar program that explicitly models changes to the heap. We model allocation using a ghost set *Alloc* corresponding to the allocated objects and update it when a new object is allocated. We reason about arbitrary changes to the heap across a function call by requiring a 'modifies' annotation from the user and adding assumptions that the fields of objects outside the modified set of a function call remain the

[3]The presentation of FWYB augments the original program P with manipulations to $\mathcal{G}$ and Br in separate stages. This is done for clarity of exposition. This may not be possible in general since we may write ghost code with expressions that use both the $\mathcal{G}$ maps and Br. However, we can combine the proofs of Propositions 3.4 and 3.5 to show the soundness of projecting out all ghost code in a single stage, and Theorem 3.8 continues to hold in the general case.

same across the call. We express these assumptions using parameterized map updates which are supported by the generalized array theory [19]. We detail this reduction in our technical report [1] in Appendix A.3.

4 ILLUSTRATIVE DATA STRUCTURES AND VERIFICATION

Intrinsic definitions and the fix-what-you-break verification methodology are new concepts that require thinking afresh about data structures and annotating methods that operate over them. In this section, we present several classical data structures and methods over them, and illustrate how the verification engineer can write intrinsic definitions (which maps to choose, and what the local conditions ensure) and how they can fix broken sets to prove programs correct.

4.1 Insertion into a Sorted List

In this section we present the verification of insertion into a sorted list implemented in the FWYB methodology in its entirety. Our running example in Section 3 illustrates the key technical ideas involved in verifying the program. In this section we present an end-to-end picture that mirrors the verification experience in practice.

Data Structure Definition. We first revise the definition of a sorted list (Example 2.6) with a different set of monadic maps. We have the following monadic maps $\mathcal{G}-$ $prev : C \to C?$, $length : C \to \mathbb{N}$, $keys : C \to Set(Int)$, $hslist : C \to Set(C)$ that model the *previous* pointer (inverse of next), length of the sorted list, the set of keys stored in it, and its heaplet (set of locations that form the sorted list) respectively. We use the length, keys, and heaplet maps to state full functional specifications of methods. The local conditions are:

$$\begin{aligned}
&\forall x.\ next(x) \neq nil \Rightarrow (\ key(x) \leq key(next(x))\ \wedge\ prev(next(x)) = x \\
&\qquad\qquad \wedge\ length(x) = 1 + length(next(x))\ \wedge\ keys(x) = \{key(x)\} \cup keys(next(x)) \\
&\qquad\qquad \wedge\ hslist(x) = \{x\} \uplus hslist(next(x))\ ) \qquad\qquad (\uplus\text{: disjoint union}) \\
&\wedge\ prev(x) \neq nil \Rightarrow next(prev(x)) = x \\
&\wedge\ next(x) = nil \Rightarrow (\ length(x) = 1\ \wedge\ keys(x) = \{key(x)\}\ \wedge\ hslist(x) = \{x\}\ )
\end{aligned} \tag{2}$$

The above definition is slightly different from the one given in Example 2.6. The *length* map replaces the *rank* map, requiring additionally that lengths of adjacent nodes differ by 1.

The *prev* map is a gadget we find useful in many intrinsic definitions. The constraints on *prev* ensure that the C-heaps satisfying the definition only contain non-merging lists. To see why this is the case, consider for the sake of contradiction distinct objects o_1, o_2, o_3 such that $next(o_1) = next(o_2) = o_3$. Then, we can see from the local conditions that we must simultaneously have $prev(o_3) = o_1$ and $prev(o_3) = o_2$, which is impossible. Finally, the *hslist* and *keys* maps represent the heaplet and the set of keys stored in the sorted list (respectively).

The heads of all sorted lists in the C-heap is then defined by the following correlation formula:

$$\varphi(y) \equiv prev(y) = nil$$

Constructing Provably Correct Impact Sets for Mutations. We now instantiate the rules developed in Section 3.5 for sorted lists. Recall that well-behaved programs must update the broken set with the impact set of a mutation. Table 1 captures the impact set for each field mutation. Note that the terms denoting the impacted objects belong to A_f only if they do not evaluate to *nil*.

Let us consider the correctness of Table 1, focusing on the mutation of *next* as an example. Figure 3 illustrates the heap after the mutation `x.next := z`. We make the following key observation: the local constraints $LC(v)$ for an object v refer only to the properties of objects v, $next(v)$, and $prev(v)$ (see 2), i.e., objects that are at most "one step" away on the heap. Therefore, the only objects that

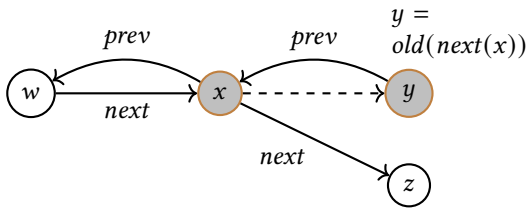


Fig. 3. Reasoning about the set of objects broken by `x.next := z`. The dashed arrow represents the old *next* pointer before the mutation. The grey nodes denote objects where local conditions can be broken by the mutation. We see that only x and y may violate *next* and *prev* being inverses.

Mutated Field f	Impacted Objects A_f
$x.next$	$\{x, old(next(x))\}$
$x.key$	$\{x, prev(x)\}$
$x.prev$	$\{x, old(prev(x))\}$
$x.hslist$	$\{x, prev(x)\}$
$x.length$	$\{x, prev(x)\}$
$x.keys$	$\{x, prev(x)\}$

Table 1. Table of impact sets corresponding to field mutations for sorted lists (See 2 in Section 4.1). $old(t)$ refers to the value of the term t before the mutation. Terms only belong to the sets if not equal to *nil*.

can be broken by the mutation `x.next := z` are those that are one step away from x either via an incoming or an outgoing edge via pointers *next* and *prev*. This is a general property of intrinsic definitions: *mutations cannot immediately affect objects that are far away on the heap.* [4]

In our case, we claim that the impact set contains at most x and $old(next(x))$. Here's a proof (see Fig 3): Consider z such that $z \neq old(next(x))$ (as there is no real mutation otherwise). If z was not broken before the mutation, then it cannot be the case that $prev(z) = x$. Looking at the local conditions, it is clear that such a z will remain unbroken after the mutation. Now consider a w not broken before the mutation such that $next(w) = x$. Then it follows from the local conditions that there can only be one such (unbroken) w, and further $w \neq x$. w's fields are not mutated, and by examining LC, it is easy to see that w will not get broken (as $LC(v)$ does not refer to $next(next(v))$). The argument is the same for w such that $prev(x) = w$. Finally, consider a y not broken before the mutation such that $prev(y) = x$. We can then see from the local conditions that $y = old(next(x))$, which is already in the impact set.

The above argument is subtle, but we can automatically check whether impact sets declared by a verification engineer are correct. The Mutation rule in Figure 2 characterizes the impact set A_{next} for mutation of the field *next* as follows:

$$\vdash \{u \neq x \wedge u \neq next(x) \wedge LC(u) \wedge x \neq nil\}\ x.next := z\ \{LC(u)\}$$

The above says that any location u that is not in the impact set which satisfied the local conditions before the mutation must continue to satisfy them after the mutation. We present the formulation for the general case in our technical report [1] in Appendix C. Finally, note that the validity of the above triple is decidable. In our realization of the FWYB methodology we prove our impact sets correct by encoding the triple in Boogie (see Section 5.3).

Macros that ensure Well-Behaved Programs. In Section 3.5 we characterized well-behaved programs as a set of syntactic rules (Figure 2). We can realize these restrictions using macros:

(1) `Mut(x,f,v,Br)` for each $f \in \mathcal{F} \cup \mathcal{G}$, which represents the sequence of statements `x.f := v; Br := Br U` A_f`(x)`. Here A_f`(x)` is the impact set corresponding to the mutation on f on x as given by the table above. This macro is used instead of `x.f := v` and automatically ensures that the impact set is added to the broken set.

[4]Note that a mutation can necessitate changes to monadic maps for an unbounded number of nodes *eventually*; however, these are not necessary immediately. As we fix monadic maps on a broken object, its neighbors could get broken and need to be fixed, leading to their neighbors breaking, etc. This can lead to a ripple effect that would eventually require an unbounded number of locations to be fixed.

(2) `NewObj(x,Br)`, which represents the statements `x := new C(); Br := Br U { x }`. This macro is used instead of `x := new C()` and ensures that any newly allocated object is automatically added to the broken set.
(3) `AssertLCAndRemove(x,Br)`, which represents the statements `assert LC(x); Br := Br \ { x }`. This macro is allowed anytime the engineer wants to assert that x satisfies the local condition, and then remove it from the broken set.[5]
(4) `InferLCOutsideBr(x, Br)`, which represents the statements `assert (x ≠ nil ∧ x ∉ Br); assume LC(x)`. This allows the engineer at any time to assert that x is not in the broken set and assume it satisfies the local condition.

The above macros correspond to the rules Mutation, Allocation, Assert LC and Remove, and Infer LC Outside Br respectively. Restricting to the syntactic fragment that contains the above macros and disallows mutation and allocation otherwise enforces the *programming discipline* that ensures well-behaved programs.

We present the full well-behaved code written using the above macros and discuss it in our technical report [1] in Appendix D.1.

4.2 Reversing a Sorted List

We return to lists for another case study: reversing a sorted list. The purpose of this example is to demonstrate how the fix-what-you-break philosophy works with iteration/loops. We augment the definition of sorted linked lists from Case Study 4.1 to make sortedness optional and determined by predicates that capture sortedness in non-descending order, with $sorted : C \rightarrow Bool$, and sortedness with non-ascending order, with $rev\_sorted : C \rightarrow Bool$. The relevant additions to the local condition and the impact sets for these monadic maps can be seen below:

$$
\begin{aligned}
&(next(x) \neq nil \Rightarrow \\
&\quad sorted(x) \Rightarrow (key(x) \leq key(next(x)) \wedge sorted(x) = sorted(next(x))) \\
&\quad \wedge\ rev\_sorted(x) \Rightarrow (key(x) \geq key(next(x)) \\
&\qquad\qquad \wedge\ rev\_sorted(x) = rev\_sorted(next(x))))
\end{aligned}
$$

Mutated Field f	Impacted Objects A_f
$sorted$	$\{x, prev(x)\}$
rev_sorted	$\{x, prev(x)\}$

We present the full local condition and code in our technical report [1] in Appendix D.3. However, the gist of the method is that we are popping C nodes off of the front of a temporary list cur, and pushing them to the front of a new reversed list ret. The method consists mainly of a loop that performs the aforementioned action repeatedly. A technique we use to verify loops using FWYB is to maintain that the broken set contains no nodes or only a finite number of nodes for which we specify how they are broken. In the case of this method, Br remains empty, as the loop maintains cur and ret as two valid lists, not modifying any other nodes. When popping x from cur and adding it to ret, in addition to repairing the new cur by setting its parent pointer to nil, we also need to update fields such as $length$ and $keys$ on x, so it satisfies the relevant local conditions as the new head of the ret list.

4.3 Circular Lists

Our next example is circular lists. This example illustrates a neat trick in FWYB that where we assert that we can reach a special node known as a *scaffolding* node, and that in addition to asserting properties on the node that is given to the method, one can also assert properties on this scaffolding node. In order to make verification of properties on this scaffolding node easier, the scaffolding node remains unchanged in the data structure, and is never deleted. We start with a data structure

[5]We extend our basic programming language defined in Figure 1 with an assert statement and give it the usual semantics (program reaches an error state if the assertion is not satisfied, but is equivalent to skip otherwise).

containing a pointer $next : C \to C$ and a monadic map $prev : C \to C$. We build on this data structure to define circular lists by adding a monadic map $last : C \to C$ where $last(u)$ for any location u points to the last item in the list, which is the scaffolding node in this case. The scaffolding node x must in turn point to another node whose $last$ map points to x itself: this ensures cyclicity. We also define monadic maps $length : C \to Nat$ and $rev\_length : C \to Nat$ to denote the distance to the $last$ node by following $prev$ or $next$ pointers. The partial local conditions for x are as below:

$$\begin{aligned}(x = last(x) \Rightarrow last(next(x)) = x \wedge length(x) = 0 \wedge rev\_length(x) = 0)\\ \wedge (x \neq last(x) \Rightarrow last(next(x)) = last(x) \wedge length(x) = length(next(x)) + 1\\ \wedge rev\_length(x) = rev\_length(prev(x)) + 1)\end{aligned}$$

Here is the gist of inserting a node at the back of a circular list. We are given a node x such that $next(x) = last(x)$ (at the end of a cycle). We insert a newly allocated node after x, making local repairs there. Then, in a ghost loop similar to the one in Case Study 4.2, we make appropriate updates to the *length* and *keys* maps, which are not fully described here, following the $prev$ map until we reach $last(x)$. Like in the previous case study, we present the full local condition and code in our technical report [1] in Appendix D.4.

4.4 Overlaid Data Structure of List and BST

One of the settings where intrinsic definitions shine is in defining and manipulating an *overlaid data structure* that overlays a linked list and a binary search tree. The list and tree share the same locations, and the *next* pointer threads them into a linked list while the *left, right* pointers on them defines a BST. Such structures are often used in systems code (such as Linux kernels) to save space [34]. For example, I/O schedulers use an overlaid structure as above, where the list/queue stores requests in FIFO order while the bst enables faster searching according requests with respect to a key. While there has been work in verification of memory safety of such structures [34], we aim here to check preservation of such data structures.

Intrinsic definition over such an overlaid data structure is pleasantly *compositional*. We simply take intrinsic definitions for lists and trees, and take the union of the monadic maps and the conjunction of their local conditions. The only thing that's left is then to ensure that they contain the same set of locations. We introduce a monadic map *bst_root* that maps every node to its root in the bst, and introduce a monadic map *list_head* that maps every node to the head of the list it belong to (using appropriate local conditions). We then demand that all locations in a list have the same *bst_root* and all locations in a tree have the same *list_head*, using local conditions. We also define monadic maps that define the bst-heaplet for tree nodes and list-heaplet for list nodes (the locations that belong to the tree under the node or the list from that node, respectively) using local conditions. We define a correlation predicate *Valid* that relates the head h of the list and root r of the tree by demanding that the bst-root of h is r and the list-head of r is h, and furthermore, the list-heaplet of h and tree-heaplet of r are equal. This predicate can be seen here:

$$Valid \equiv \ bst\_root(h) = r \wedge list\_root(r) = h \wedge \ list\_heaplet(h) = bst\_heaplet(r)$$

We prove certain methods manipulating this overlaid structure correct (such as deleting the first element of the list and removing it both from the list as well as the BST). These ghost annotations are mostly compositional— with exceptions for fields whose mutation impacts the local condition of multiple components, we fix monadic maps for the BST component in the same way we fix them for stand-alone BSTs and fix monadic maps for the list component in the same way we fix them for stand-alone lists. In fact, we maintain two broken sets, one for BST and one for list, as updating a pointer for BST often doesn't break the local property for lists, and vice versa.

Limitations. In modeling the data structures above, we crucially used the fact that for any location, there is at most one location (or a bounded number of locations) that has a field pointing to this location. We used this fact to define an inverse pointer (*prev* or *parent*/*p*), which allows us to capture the impact set when a location's fields are mutated. Consequently, we do not know how to model structures where locations can have unbounded indegree. We could model these inverse pointers using a sequence/array of pointers, but verification may get more challenging. Data structures with unbounded outdegree can however be modeled using just a linked-list of pointers and hence seen as a structure with bounded outdegree.

5 IMPLEMENTATION AND EVALUATION

5.1 Implementation Strategy of IDS and FWYB in Boogie

We implement the technique of intrinsically defined data structures and FWYB verification in the program verifier Boogie [7]. Boogie is a low-level imperative programming language which supports systematic generation of verification conditions that are checked using SMT solvers.

We choose Boogie as it is a low-level verification condition generator. We expect that scalar programs with quantifier-free specifications, annotations, and invariants, and given our careful modeling of the heap and its modification across function calls (Section 3.7), reduces to quantifier-free verification conditions that fall into decidable logics. We further cross-check that our encodings indeed generate decidable queries by checking the generated SMT files. Furthermore, a plethora of higher-level languages compile to Boogie (e.g., VCC and Havoc for C [16, 17], Dafny [35] with compilation to .NET, Civl for concurrent programs [28], Move for smart contracts [20], etc.). Implementing a technique in Boogie hence shows a pathway for implementing IDS and FWYB for higher-level languages as well.

Modeling Fix-What-You-Break Verification in Boogie. We model heaps in Boogie by having a sort *Loc* of locations and modeling pointers as maps from *Loc* to sorts. We implement monadic maps also as maps from locations to field values. We implement our benchmarks using the *macros* for well-behaved programming defined in Section 4.1. We implement allocation with an *Alloc* set and heap change across function calls using parameterized map updates as described in Section 3.7 and our technical report [1] in Appendix A.3.

We ensure that the VCs generated by Boogie fall into decidable fragments, and there are several components that ensure this. First, note that all specifications (contracts and invariants) are quantifier-free. Second, pure functions (used to implement local conditions) are typically encoded using quantification, but we ensure Boogie inlines them to avoid quantification. Third, heap updates that are the effect of procedures and set operations for set-valued monadic maps are modeled using parameterized map updates [19], which Boogie supports natively. Finally, we cross-check that the generated SMT query is quantifier-free and decidable by checking the absence of statements that introduce quantified reasoning, including `exists`, `forall`, and `lambda`.

5.2 Benchmarks

We evaluate our technique on a variety of data structures and methods that manipulate them. Our benchmark suite consists of data structure manipulation methods for a variety of different list and tree data structures, including sorted lists, circular lists, binary search trees, and balanced binary search trees such as Red-Black trees and AVL trees. Methods include core functionality such as search, insertion and deletion. The suite includes an *overlaid* data structure that overlays a binary search tree and a linked list, implementing methods needed by a simplified version of the Linux deadline IO scheduler [34]. The contracts for these functions are complete functional specifications

Data Structure	*LC* Size	Method	LOC+Spec +Ann	Verif. Time(s)	Method	LOC+Spec +Ann	Verif. Time(s)
Singly-Linked List	8	Append	4+11+10	2.0	Insert-Back	6+13+12	2.0
		Copy-All	7+8+9	2.0	Insert-Front	3+13+7	2.0
		Delete-All	10+9+16	2.0	Insert	9+13+23	2.0
		Find	4+4+2	1.9	Reverse	6+8+18	2.1
Sorted List	14	Delete-All	10+9+16	2.1	Merge	11+9+20	2.1
		Find	4+4+2	1.9	Reverse	5+14+22	2.1
		Insert	9+16+27	2.1			
Sorted List (w. *min*, *max* maps)	20	Concatenate	6+10+13	2.2	Find-Last	5+10+9	2.0
Circular List	27	Insert-Front	4+12+41	2.3	Delete-Front	3+12+39	2.4
		Insert-Back	5+14+45	2.4	Delete-Back	3+13+55	2.4
Binary Search Tree	35	Find	4+3+5	2.0	Delete	10+13+30	2.8
		Insert	9+12+37	2.7	Remove-Root	17+15+47	3.8
Treap	37	Find	4+3+5	2.0	Delete	10+13+30	3.1
		Insert	19+12+74	10.2	Remove-Root	24+15+74	5.4
AVL Tree	45	Insert	12+12+36	5.1	Find-Min	5+5+8	2.1
		Delete	43+13+62	5.3	Balance	40+17+95	5.0
Red-Black Tree	48	Insert	76+12+203	74.1	Del-L-Fixup	33+20+93	8.9
		Delete	56+13+76	5.8	Del-R-Fixup	33+20+93	7.4
		Find-Min	5+5+8	2.1			
BST+Scaffolding	59	Delete-Inside	1+24+51	4.8	Remove-Root	44+31+61	10.2
Scheduler Queue (overlaid SLL+BST)	72	Move-Request	4+10+8	2.9	BST-Delete-Inside	1+29+55	4.9
		List-Remove-First	5+13+10	2.7	BST-Remove-Root	44+36+65	15.0

Table 2. Implementation and verification of Boogie programs on the benchmarks. The columns give data structure, size of local conditions for capturing the datatructure as number of conjuncts, method, lines of executable code in the method, lines of specification (pre/post), lines of ghost code annotations (invariants/monadic map updates), and verification time in seconds.

that not only ask for maintenance of the data structure, but correctness properties involving the returned values, the keys stored in the container, and the heaplet of the data structure.

5.3 Evaluation

We first evaluate the following two research questions:

RQ1: Can the data structures be expressed using IDS, and can the FWYB methodology for methods on these structures be expressed in Boogie?

RQ2: Is Boogie with decidable verification condition generation dispatched to SMT solvers effective in verifying these methods?

As we have articulated earlier, intrinsic definitions and monadic map updates require a new way of thinking about programs and repairs. We implement the specifications using monadic maps and local conditions, and the benchmarks using the well-behavedness macros and ghost updates. We were able to express all data structures and FWYB annotations for the methods on these structures for our benchmarks in Boogie (RQ1). Importantly, we were able to write quantifier-free modular contracts for the auxiliary methods and loop invariants using the monadic maps and strengthening the contracts using quantifier-free assertions on broken sets (which may not be empty for auxiliary methods). We do not prove termination for these methods except for ghost loops and ghost recursive procedures (termination for latter is required for soundness). We provide the benchmarks with annotations in an anonymized repository[6].

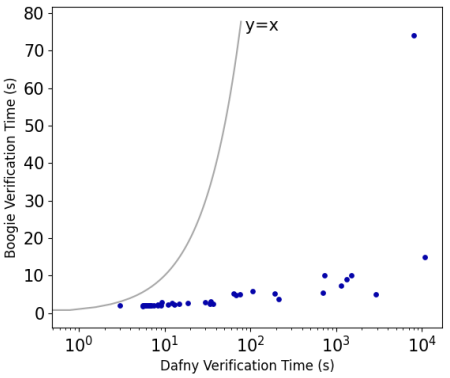
[6] https://zenodo.org/records/10807070

Our annotation measures and verification results are detailed in the table in Table 2, for 42 methods across 10 data structure definitions. These measurements were taken from a machine with an Intel™ Core i5-4460 processor at 3.20 GHz. We found the verification performance excellent overall (RQ2): all the methods verify in under 2 minutes, and all but four verify in under 10 seconds. We used the option that sets the maximum number of VC splits to 8 in Boogie. The times reported for each method are the sum of times taken for the following steps: verifying that the impact sets are correct (<3s for all data structures), generating verification conditions with Boogie, injecting parametric update implementations, and solving the SMT queries.

Notice that the lines of ghost code written is nontrivial, but these are typically simple, involving programmatically repairing monadic maps and manipulating broken sets. In fact, a large fraction (~ 60%) of ghost updates in our benchmarks were *definitional updates* that simply update a field according to its definition in the local condition. An example is updating $x.length$ to $x.next.length+1$ for lists. We believe that the annotation burden can be significantly lowered in future work by automating such updates. More importantly, note that none of the programs required further annotations like instantiations, triggers, inductive lemmas, etc. in order to prove them correct.

RQ3: What is the performance impact of generating decidable verification conditions?

In order to study this, we implemented the entire benchmark suite described in Table 2 in Dafny, a higher-level programming language that uses Boogie to perform its verification. We implemented the data structures and the FWYB methodology identically in Dafny as the Boogie version. Even though our annotations are all quantifier-free, Dafny generates Boogie code where several aspects of the language, in particular allocation and heap change across function calls, are modeled using *quantifiers*, resulting in quantified queries to SMT solvers. The scatter plot on the right shows the performance of Boogie and Dafny on the benchmarks. The plot clearly strongly suggests that even though Dafny is able to prove the FWYB-annotated programs correct, using decidable verification conditions results in much better performance. We hence believe that implementing program verifiers (such as Dafny) that exploit the fact that FWYB annotations can be compiled to annotations in Boogie that result in decidable VCs is a promising future direction to achieve faster high-level IDS+FWYB frameworks.



6 RELATED WORK

There have been mainly two paradigms to automated verification of programs annotated with rich contracts written in logic. The first is to restrict the specification logic so that verification conditions fall into a decidable logic. The second allows validity of verification conditions to fall into an undecidable or even an incomplete logic (where validity is not even recursively enumerable), but support effective strategies nevertheless, using heuristics, lemma synthesis, and further annotations from the programmer [3–6, 10, 11, 13–15, 21, 26, 44, 48, 49, 52, 54, 57, 61]. In this paper, we have proposed a new paradigm of predictable verification that calls for programmers to write a reasonable amount of extra annotations under which validity of verification conditions becomes decidable. To the best of our knowledge, we do not know of any other work of this style (where validity of verification conditions is undecidable but an upfront set of annotations renders it decidable).

Decidable verification. There is a rich body of research on decidable logics for heap verification: first-order logics with reachability [36], the logic LISBQ in the HAVOC tool [32], several decidable fragments of separation logic known [9, 53] as well as fragments that admit a decidable entailment problem [23]. Decidable logics based on interpreting bounded treewidth data structures on trees have also been studied, for separation logics as well as other logics [25, 39, 40]. In general, these logics are heavily restricted— the magic wand in separation logic quickly leads to undecidability [12], the general entailment problem for separation logic with inductive predicates is undecidable [2], and validity of first-order logic with recursive definitions is undecidable and not even recursively enumerable and does not admit complete proof procedures.

Validity checking of undecidable and incomplete logics. Heap verification using undecidable and incomplete logics has been extensively in the literature. The work on natural proofs [37, 52] for imperative programs and work on Liquid Types [59] for functional programs propose such approaches that utilize SMT solvers, but require extra user help in the form of inductive lemmas to verify programs. Users need to understand the underlying heuristic SMT encoding mechanisms and their shortcomings, as well as theoretical shortcomings (the difference between fixed point and least fixed point semantics of recursive definitions) in order to provide these lemmas (see [37, 44, 45]). In contrast, the user help we seek in this work is upfront ghost code that updates monadic maps to satisfy local conditions independently of the heuristics the solvers use. Furthermore, for programs with such annotations, we assure decidable validation of the associated verification conditions.

Monadic Maps. Monadic maps have been exploited in earlier work in other forms for simplifying verification of properties of global structures. In shape analysis [60], monadic predicates are often used to express inductively defined properties of single locations on the heap. In separation logic, the *iterated separating conjunction operator*, introduced already by Reynolds in 2002 [58], expresses local properties of each location, and is akin to monadic maps. Iterated separation conjunction has been used in verification, for both arrays as well as for data structures, in various forms [22, 43]. The work on verification using flows [29–31, 41, 42, 51] introduces predicates based on flows, and utilizes such predicates in iterated separation formulas to express global properties of data structures and to verify algorithms such as the concurrent Harris list. In these works, local properties of locations and proof systems based on them are explored, but we do not know of any work exploiting monadic maps for decidable reasoning, which is crucial for predictable verification.

Ghost code. The methodology of writing ghost code is a common paradigm in deductive program verification [24, 27, 38, 56] and supported by verification tools such as BOOGIE and DAFNY [7, 35]. Ghost code involves code that manipulates auxiliary variables to perform a parallel computation with the original code without affecting it. Our use of ghost code establishes the required monadic maps that satisfy local conditions by allowing the programmer to construct the maps and verify the local conditions using a disciplined programming methodology. Furthermore, we assure that the original code with the ghost code results in decidable verification problems, which is a salient feature not found typically in other contexts where ghost code is used.

7 CONCLUSIONS

We introduced intrinsic definitions that eschew recursion/induction and instead define data structures using monadic maps and local conditions. Proving that a program maintains a valid data structure hence requires only maintaining monadic maps and verifying the local conditions on locations that get broken. Furthermore, verifying that engineer-provided ghost code annotations are indeed correct falls into decidable theories, leading to a predictable verification framework.

Future Work. First, it would be useful to develop verification engines for higher-level languages (like Java [26], Rust [33], and Dafny [35]) that that have native support for intrinsic definitions

and produce verification conditions in decidable theories that SMT solvers can handle (see RQ3 in Section 5.3). Second, it would be interesting to see how intrinsic definitions with fix-what-you-break proof methodology can coexist and exchange information with traditional recursive definitions with induction-based proof methodology. Third, as mentioned in Section 5.3, many updates of monadic maps are straightforward using definitions, and tools that automate this can reduce annotation burden significantly. Fourth, we are particularly intrigued with the ease with which intrinsic data structures capture more complex data structures such as overlaid data structures. Exploring intrinsic definitions for verifying concurrent and distributed programs that maintain data structures is particularly interesting. Fifth, intrinsic definitions opens up an entirely new approach to defining properties of structures that simplify reasoning. We believe that exploiting intrinsic definitions in other verification contexts, like mathematical structures used in specifications (e.g., message queues in distributed programs), parameterized concurrent programs (configurations modeled as unbounded sequences of states), and programs that manipulate big data concurrently (like Apache Spark) are exciting future directions. Finally, it would be interesting to adapt IDS for functional programs. Since functional data structures are not mutable, ghost fields will always meet local conditions. However, we may need to *(re-)establish* rather than *repair* local conditions, which may require ghost code, e.g., establishing that the ghost map *sorted* on a functional list x is true.

REFERENCES

[1] 2024. Predictable Verification using Intrinsic Defintitions (Technical Report) (Placeholder Reference, will be uploaded to arxiv).

[2] Timos Antonopoulos, Nikos Gorogiannis, Christoph Haase, Max Kanovich, and Joël Ouaknine. 2014. Foundations for Decision Problems in Separation Logic with General Inductive Predicates. In *Foundations of Software Science and Computation Structures*, Anca Muscholl (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 411–425.

[3] Anindya Banerjee, Mike Barnett, and David A. Naumann. 2008. Boogie Meets Regions: A Verification Experience Report. In *Verified Software: Theories, Tools, Experiments*, Natarajan Shankar and Jim Woodcock (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 177–191.

[4] Anindya Banerjee and David A. Naumann. 2013. Local Reasoning for Global Invariants, Part II: Dynamic Boundaries. *J. ACM* 60, 3, Article 19 (jun 2013), 73 pages. https://doi.org/10.1145/2485981

[5] Anindya Banerjee, David A. Naumann, and Stan Rosenberg. 2008. Regional Logic for Local Reasoning about Global Invariants. In *ECOOP 2008 – Object-Oriented Programming*, Jan Vitek (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 387–411.

[6] Anindya Banerjee, David A. Naumann, and Stan Rosenberg. 2013. Local Reasoning for Global Invariants, Part I: Region Logic. *J. ACM* 60, 3, Article 18 (June 2013), 56 pages. http://doi.acm.org/10.1145/2485982

[7] Mike Barnett, Bor-Yuh Evan Chang, Robert DeLine, Bart Jacobs, and K. Rustan M. Leino. 2006. Boogie: A Modular Reusable Verifier for Object-Oriented Programs. In *Formal Methods for Components and Objects*, Frank S. de Boer, Marcello M. Bonsangue, Susanne Graf, and Willem-Paul de Roever (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 364–387.

[8] Clark Barrett, Christopher L. Conway, Morgan Deters, Liana Hadarean, Dejan Jovanović, Tim King, Andrew Reynolds, and Cesare Tinelli. 2011. CVC4. In *Computer Aided Verification*, Ganesh Gopalakrishnan and Shaz Qadeer (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 171–177.

[9] Josh Berdine, Cristiano Calcagno, and Peter W. O'Hearn. 2005. A Decidable Fragment of Separation Logic. In *FSTTCS 2004: Foundations of Software Technology and Theoretical Computer Science*, Kamal Lodaya and Meena Mahajan (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 97–109.

[10] Josh Berdine, Cristiano Calcagno, and Peter W. O'Hearn. 2005. Symbolic Execution with Separation Logic. In *Programming Languages and Systems*, Kwangkeun Yi (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 52–68.

[11] Josh Berdine, Cristiano Calcagno, and Peter W. O'Hearn. 2006. Smallfoot: Modular Automatic Assertion Checking with Separation Logic. In *Proceedings of the 4th International Conference on Formal Methods for Components and Objects* (Amsterdam, The Netherlands) *(FMCO'05)*. Springer-Verlag, Berlin, Heidelberg, 115–137. https://doi.org/10.1007/11804192_6

[12] Rémi Brochenin, Stéphane Demri, and Etienne Lozes. 2008. On the Almighty Wand. In *Computer Science Logic*, Michael Kaminski and Simone Martini (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 323–338.

[13] Cristiano Calcagno, Dino Distefano, Peter W. O'Hearn, and Hongseok Yang. 2011. Compositional Shape Analysis by Means of Bi-Abduction. *J. ACM* 58, 6, Article 26 (dec 2011), 66 pages. https://doi.org/10.1145/2049697.2049700

[14] Wei-Ngan Chin, Cristina David, Huu Hai Nguyen, and Shengchao Qin. 2007. Automated Verification of Shape, Size and Bag Properties. In *Proceedings of the 12th IEEE International Conference on Engineering Complex Computer Systems (ICECCS '07)*. IEEE Computer Society, USA, 307–320. https://doi.org/10.1109/ICECCS.2007.17

[15] Duc-Hiep Chu, Joxan Jaffar, and Minh-Thai Trinh. 2015. Automatic Induction Proofs of Data-Structures in Imperative Programs. In *Proceedings of the 36th ACM SIGPLAN Conference on Programming Language Design and Implementation* (Portland, OR, USA) *(PLDI '15)*. Association for Computing Machinery, New York, NY, USA, 457–466. https://doi.org/10.1145/2737924.2737984

[16] Ernie Cohen, Markus Dahlweid, Mark Hillebrand, Dirk Leinenbach, Michał Moskal, Thomas Santen, Wolfram Schulte, and Stephan Tobies. 2009. VCC: A Practical System for Verifying Concurrent C. In *Theorem Proving in Higher Order Logics*, Stefan Berghofer, Tobias Nipkow, Christian Urban, and Makarius Wenzel (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 23–42.

[17] Jeremy Condit, Brian Hackett, Shuvendu K. Lahiri, and Shaz Qadeer. 2009. Unifying Type Checking and Property Checking for Low-Level Code. *SIGPLAN Not.* 44, 1 (jan 2009), 302–314. https://doi.org/10.1145/1594834.1480921

[18] Leonardo De Moura and Nikolaj Bjørner. 2008. Z3: An Efficient SMT Solver. In *Proceedings of the 14th International Conference on Tools and Algorithms for the Construction and Analysis of Systems* (Budapest, Hungary) *(TACAS'08)*. Springer-Verlag, Berlin, Heidelberg, 337–340.

[19] Leonardo de Moura and Nikolaj Bjørner. 2009. Generalized, efficient array decision procedures. In *2009 Formal Methods in Computer-Aided Design*. IEEE, 45–52. https://doi.org/10.1109/FMCAD.2009.5351142

[20] David Dill, Wolfgang Grieskamp, Junkil Park, Shaz Qadeer, Meng Xu, and Emma Zhong. 2022. Fast and Reliable Formal Verification of Smart Contracts with the Move Prover. In *Tools and Algorithms for the Construction and Analysis of Systems*, Dana Fisman and Grigore Rosu (Eds.). Springer International Publishing, Cham, 183–200.

[21] Dino Distefano, Peter W. O'Hearn, and Hongseok Yang. 2006. A Local Shape Analysis Based on Separation Logic. In *Tools and Algorithms for the Construction and Analysis of Systems*, Holger Hermanns and Jens Palsberg (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 287–302.

[22] Dino Distefano and Matthew Parkinson. 2008. jStar: Towards Practical Verification for Java. *Sigplan Notices - SIGPLAN* 43, 213–226. https://doi.org/10.1145/1449764.1449782

[23] Mnacho Echenim, Radu Iosif, and Nicolas Peltier. 2021. Unifying Decidable Entailments in Separation Logic with Inductive Definitions. In *Automated Deduction – CADE 28*, André Platzer and Geoff Sutcliffe (Eds.). Springer International Publishing, Cham, 183–199.

[24] Jean-Christophe Filliâtre, Léon Gondelman, and Andrei Paskevich. 2016. The spirit of ghost code. *Formal Methods in System Design* 48 (2016), 152–174.

[25] Radu Iosif, Adam Rogalewicz, and Jiri Simacek. 2013. The Tree Width of Separation Logic with Recursive Definitions. In *Automated Deduction – CADE-24*, Maria Paola Bonacina (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 21–38.

[26] Bart Jacobs, Jan Smans, Pieter Philippaerts, Frédéric Vogels, Willem Penninckx, and Frank Piessens. 2011. VeriFast: A Powerful, Sound, Predictable, Fast Verifier for C and Java. In *Proceedings of the Third International Conference on NASA Formal Methods* (Pasadena, CA) *(NFM'11)*. Springer-Verlag, Berlin, Heidelberg, 41–55.

[27] C. B. Jones. 2010. The Role of Auxiliary Variables in the Formal Development of Concurrent Programs. In *Reflections on the Work of C.A.R. Hoare*, A.W. Roscoe, Cliff B. Jones, and Kenneth R. Wood (Eds.). Springer London, London, 167–187. https://doi.org/10.1007/978-1-84882-912-1_8

[28] Bernhard Kragl and Shaz Qadeer. 2021. The Civl Verifier. In *2021 Formal Methods in Computer Aided Design (FMCAD)*. 143–152. https://doi.org/10.34727/2021/isbn.978-3-85448-046-4_23

[29] Siddharth Krishna, Nisarg Patel, Dennis Shasha, and Thomas Wies. 2020. Verifying Concurrent Search Structure Templates. In *Proceedings of the 41st ACM SIGPLAN Conference on Programming Language Design and Implementation* (London, UK) *(PLDI 2020)*. Association for Computing Machinery, New York, NY, USA, 181–196. https://doi.org/10.1145/3385412.3386029

[30] Siddharth Krishna, Dennis E. Shasha, and Thomas Wies. 2018. Go with the flow: compositional abstractions for concurrent data structures. *Proc. ACM Program. Lang.* 2, POPL (2018), 37:1–37:31. https://doi.org/10.1145/3158125

[31] Siddharth Krishna, Alexander J. Summers, and Thomas Wies. 2020. Local Reasoning for Global Graph Properties. In *Programming Languages and Systems - 29th European Symposium on Programming, ESOP 2020, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2020, Dublin, Ireland, April 25-30, 2020, Proceedings (Lecture Notes in Computer Science, Vol. 12075)*, Peter Müller (Ed.). Springer, 308–335. https://doi.org/10.1007/978-3-030-44914-8_12

[32] Shuvendu Lahiri and Shaz Qadeer. 2008. Back to the Future: Revisiting Precise Program Verification Using SMT Solvers. *SIGPLAN Not.* 43, 1 (jan 2008), 171–182. https://doi.org/10.1145/1328897.1328461

[33] Andrea Lattuada, Travis Hance, Chanhee Cho, Matthias Brun, Isitha Subasinghe, Yi Zhou, Jon Howell, Bryan Parno, and Chris Hawblitzel. 2023. Verus: Verifying Rust Programs Using Linear Ghost Types. *Proc. ACM Program. Lang.* 7, OOPSLA1, Article 85 (apr 2023), 30 pages. https://doi.org/10.1145/3586037

[34] Oukseh Lee, Hongseok Yang, and Rasmus Petersen. 2011. Program Analysis for Overlaid Data Structures. In *Computer Aided Verification*, Ganesh Gopalakrishnan and Shaz Qadeer (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 592–608.

[35] K Rustan M Leino. 2010. Dafny: An automatic program verifier for functional correctness. In *International conference on logic for programming artificial intelligence and reasoning*. Springer, 348–370.

[36] Tal Lev-Ami, Neil Immerman, Thomas Reps, Mooly Sagiv, Siddharth Srivastava, and Greta Yorsh. 2009. Simulating reachability using first-order logic with applications to verification of linked data structures. *Logical Methods in Computer Science* 5 (04 2009). https://doi.org/10.2168/LMCS-5(2:12)2009

[37] Christof Löding, P. Madhusudan, and Lucas Peña. 2018. Foundations for natural proofs and quantifier instantiation. *PACMPL* 2, POPL (2018), 10:1–10:30. https://doi.org/10.1145/3158098

[38] P Lucas. 1968. *Two constructive realizations of the block concept and their equivalence, IBM Lab.* Technical Report. Vienna TR 25.085.

[39] P. Madhusudan, Gennaro Parlato, and Xiaokang Qiu. 2011. Decidable Logics Combining Heap Structures and Data. *SIGPLAN Not.* 46, 1 (jan 2011), 611–622. https://doi.org/10.1145/1925844.1926455

[40] P. Madhusudan and Xiaokang Qiu. 2011. Efficient Decision Procedures for Heaps Using STRAND. In *Static Analysis*, Eran Yahav (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 43–59.

[41] Roland Meyer, Thomas Wies, and Sebastian Wolff. 2022. A Concurrent Program Logic with a Future and History. *Proc. ACM Program. Lang.* 6, OOPSLA2, Article 174 (oct 2022), 30 pages. https://doi.org/10.1145/3563337

[42] Roland Meyer, Thomas Wies, and Sebastian Wolff. 2023. Make Flows Small Again: Revisiting the Flow Framework. In *Tools and Algorithms for the Construction and Analysis of Systems: 29th International Conference, TACAS 2023, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2023, Paris, France, April 22–27, 2023, Proceedings, Part I* (Paris, France). Springer-Verlag, Berlin, Heidelberg, 628–646. https://doi.org/10.1007/978-3-031-30823-9_32

[43] Peter Müller, Malte Schwerhoff, and Alexander J. Summers. 2016. Automatic Verification of Iterated Separating Conjunctions Using Symbolic Execution. In *Computer Aided Verification*, Swarat Chaudhuri and Azadeh Farzan (Eds.). Springer International Publishing, Cham, 405–425.

[44] Adithya Murali, Lucas Peña, Eion Blanchard, Christof Löding, and P. Madhusudan. 2022. Model-Guided Synthesis of Inductive Lemmas for FOL with Least Fixpoints. *Proc. ACM Program. Lang.* 6, OOPSLA2, Article 191 (oct 2022), 30 pages. https://doi.org/10.1145/3563354

[45] Adithya Murali, Lucas Peña, Ranjit Jhala, and P. Madhusudan. 2023. Complete First-Order Reasoning for Properties of Functional Programs. *Proc. ACM Program. Lang.* 7, OOPSLA2, Article 259 (oct 2023), 30 pages. https://doi.org/10.1145/3622835

[46] Charles Gregory Nelson. 1980. *Techniques for Program Verification.* Ph. D. Dissertation. Stanford University, Stanford, CA, USA. AAI8011683.

[47] Greg Nelson and Derek C. Oppen. 1979. Simplification by Cooperating Decision Procedures. *ACM Trans. Program. Lang. Syst.* 1, 2 (oct 1979), 245–257. https://doi.org/10.1145/357073.357079

[48] Huu Hai Nguyen and Wei-Ngan Chin. 2008. Enhancing Program Verification with Lemmas. In *Proceedings of the 20th International Conference on Computer Aided Verification* (Princeton, NJ, USA) *(CAV '08)*. Springer-Verlag, Berlin, Heidelberg, 355–369. https://doi.org/10.1007/978-3-540-70545-1_34

[49] Peter W. O'Hearn. 2012. A Primer on Separation Logic (and Automatic Program Verification and Analysis). In *Software Safety and Security - Tools for Analysis and Verification*, Tobias Nipkow, Orna Grumberg, and Benedikt Hauptmann (Eds.). NATO Science for Peace and Security Series - D: Information and Communication Security, Vol. 33. IOS Press, 286–318. https://doi.org/10.3233/978-1-61499-028-4-286

[50] Peter W. O'Hearn, John C. Reynolds, and Hongseok Yang. 2001. Local Reasoning About Programs That Alter Data Structures. In *Proceedings of the 15th International Workshop on Computer Science Logic (CSL '01)*. Springer-Verlag, London, UK, UK, 1–19. http://dl.acm.org/citation.cfm?id=647851.737404

[51] Nisarg Patel, Siddharth Krishna, Dennis Shasha, and Thomas Wies. 2021. Verifying Concurrent Multicopy Search Structures. *Proc. ACM Program. Lang.* 5, OOPSLA, Article 113 (oct 2021), 32 pages. https://doi.org/10.1145/3485490

[52] Edgar Pek, Xiaokang Qiu, and P. Madhusudan. 2014. Natural Proofs for Data Structure Manipulation in C Using Separation Logic. *SIGPLAN Not.* 49, 6 (jun 2014), 440–451. https://doi.org/10.1145/2666356.2594325

[53] Ruzica Piskac, Thomas Wies, and Damien Zufferey. 2013. Automating Separation Logic Using SMT. In *Proceedings of the 25th International Conference on Computer Aided Verification* (Saint Petersburg, Russia) *(CAV'13)*. Springer-Verlag, Berlin, Heidelberg, 773–789. https://doi.org/10.1007/978-3-642-39799-8_54

[54] Ruzica Piskac, Thomas Wies, and Damien Zufferey. 2014. Automating Separation Logic with Trees and Data. In *Proceedings of the 16th International Conference on Computer Aided Verification (CAV'14)*. Springer-Verlag, Berlin, Heidelberg, 711–728.
[55] Shaz Qadeer. 2023. Boogie Pull Request #669: Monomorphization of polymorphic maps and binders. https://github.com/boogie-org/boogie/pull/669
[56] John C. Reynolds. 1981. *The craft of programming*. Prentice Hall.
[57] John C. Reynolds. 2002. Separation Logic: A Logic for Shared Mutable Data Structures. In *Proceedings of the 17th Annual IEEE Symposium on Logic in Computer Science (LICS '02)*. IEEE Computer Society, USA, 55–74.
[58] John C. Reynolds. 2002. Separation Logic: A Logic for Shared Mutable Data Structures. In *Proceedings of the 17th Annual IEEE Symposium on Logic in Computer Science (LICS '02)*. IEEE Computer Society, USA, 55–74.
[59] Patrick M. Rondon, Ming Kawaguci, and Ranjit Jhala. 2008. Liquid types. *SIGPLAN Not.* 43, 6 (jun 2008), 159–169. https://doi.org/10.1145/1379022.1375602
[60] Mooly Sagiv, Thomas Reps, and Reinhard Wilhelm. 2002. Parametric Shape Analysis via 3-Valued Logic. *ACM Trans. Program. Lang. Syst.* 24, 3 (may 2002), 217–298. https://doi.org/10.1145/514188.514190
[61] Quang-Trung Ta, Ton Chanh Le, Siau-Cheng Khoo, and Wei-Ngan Chin. 2016. Automated Mutual Explicit Induction Proof in Separation Logic. In *FM 2016: Formal Methods*, John Fitzgerald, Constance Heitmeyer, Stefania Gnesi, and Anna Philippou (Eds.). Springer International Publishing, Cham, 659–676. https://doi.org/10.1007/978-3-319-48989-6_40
[62] Cesare Tinelli and Calogero G. Zarba. 2004. Combining Decision Procedures for Sorted Theories. In *Logics in Artificial Intelligence*, Jóse Júlio Alferes and João Leite (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 641–653.

A DETAILS FOR SECTION 3

A.1 Operational Semantics

$$\bot \xrightarrow{*} \bot$$

$$(s, O, I) \xrightarrow{\text{skip}} (s, O, I)$$

$$(s, O, I) \xrightarrow{x := nil} (s[x \mapsto nil], O, I)$$

$$(s, O, I) \xrightarrow{x := y} (s[x \mapsto s(y)], O, I)$$

$$(s, O, I) \xrightarrow{v := be} (s[v \mapsto e], O, I) \qquad \text{where } be \text{ interprets to } e \text{ according to } s \text{ and } I$$

$$(s, O, I) \xrightarrow{y := x.f} (s[y \mapsto I(f, s(x))], O, I) \quad \text{if } (f, s(x)) \in dom(I) \qquad \text{(similarly for } v := x.d\text{)}$$

$$(s, O, I) \xrightarrow{y := x.f} \bot \quad \text{if } (f, s(x)) \notin dom(I) \qquad \text{(similarly for } v := x.d\text{)}$$

$$(s, O, I) \xrightarrow{x.f := y} (s, O, I[(f, s(x)) \mapsto s(y)]) \quad \text{if } (f, s(x)) \in dom(I) \qquad \text{(similarly for } x.d := v\text{)}$$

$$(s, O, I) \xrightarrow{x.f := y} \bot \quad \text{if } (f, s(x)) \notin dom(I) \qquad \text{(similarly for } x.d := v\text{)}$$

$$(s, O, I) \xrightarrow{x := \text{new } C()} (s[x \mapsto o], O \cup \{o\}, I[(f, o) \mapsto default_f])$$
$$\text{for some } o \in \mathbb{N} \text{ such that } o \notin O$$

$$(s, O, I) \xrightarrow{\overline{r} := Function(\overline{t})} (s[\overline{r} \mapsto s'(\overline{n})], O', I') \quad \text{if } (\emptyset[\overline{m} \mapsto s(\overline{t})], O, I) \xrightarrow{Q(\overline{m},\, ret:\, \overline{n})} (s', O', I')$$
$$\text{where } Q(\overline{m},\ ret:\ \overline{n}) \text{ is the code of the method } Function,$$
$$\text{with } \overline{m} \text{ and } \overline{n} \text{ being the formal input and output parameters for } Q$$

$$(s, O, I) \xrightarrow{\text{assume } cond} (s, O, I) \quad \text{if } cond \text{ interprets to } True \text{ according to } s \text{ and } I$$

$$(s, O, I) \xrightarrow{P_1\,;P_2} (s'', O'', I'') \quad \text{if } (s, O, I) \xrightarrow{P_1} (s', O', I')$$
$$\text{and } (s', O', I') \xrightarrow{P_2} (s'', O'', I'') \text{ for some } (s', O', I')$$

$$(s, O, I) \xrightarrow{\text{if } cond \text{ then } P_1 \text{ else } P_2} (s', O', I') \quad \text{if } (s, O, I) \xrightarrow{\text{assume } cond\,;\, P_1} (s', O', I')$$

$$(s, O, I) \xrightarrow{\text{if } cond \text{ then } P_1 \text{ else } P_2} (s', O', I') \quad \text{if } (s, O, I) \xrightarrow{\text{assume } \neg cond\,;\, P_2} (s', O', I')$$

$$(s, O, I) \xrightarrow{\text{while cond do } P} (s', O', I') \quad \text{if } (s, O, I) \xrightarrow{\text{assume } cond\,;\, P\,;\, \text{while cond do } P} (s', O', I')$$

$$(s, O, I) \xrightarrow{\text{while cond do } P} (s, O, I) \quad \text{if } (s, O, I) \xrightarrow{\text{assume } \neg cond} (s, O, I)$$

Fig. 5. Operational Semantics

We give the formal operational semantics for programs in our language (Figure 1) in Figure 5 below.

Configurations are of the form (s, O, I) where $O \subset_{finite} \mathbb{N}$ represents the set of allocated objects, s represents the store and interprets program variables, and I represents the heap and interprets mutable fields in $\mathcal{F}$—including ghost fields $\mathcal{G}$ when they are used— on O (interpretations are total).

Although formally s and I are a family of functions indexed by the sorts of the variables (resp. signatures of the maps), we abuse notation and use $s(x)$ to denote the interpretation of a variable x, and similarly $I(f, o)$ to denote the value of the field f on the object o in the configuration. We add a sink state $\perp$ to model error.

Our language is safe, (i.e., allocated locations cannot point to un-allocated locations) and garbage-collected. The operational semantics is the usual one for such programs. Figure 5 presents a simplified operational semantics without considering return statements. The full semantics adds a marker to signify completion of a procedure. Procedures can only end after return statements (we syntactically disallow statements after a return) or at the end of a program.

The rules for assignments, skip, sequencing, conditionals, and loops are trivial. De-referencing a variable that does not point to an object (i.e., is *nil*) leads to the error state $\perp$. Allocation ensures memory safety by assigning the value of a field f on a newly allocated object to a constant $default_f$. For pointer fields this value is *nil*. Finally, we define the operational semantics for function calls using summaries.

A.2 Ghost Code

$$
\begin{aligned}
P \coloneqq\ & x \coloneqq Expr[Var_U, \mathcal{F}] \mid y \coloneqq x.f \mid x.f \coloneqq y \mid z \coloneqq \mathsf{new}\ C() \\
& \mid \overline{r} \coloneqq Func(\overline{t}) \qquad \overline{r}, \overline{t} \text{ are variables in } Var_U \cup Var_G \\
& \text{(Functions can have ghost input/output parameters)} \\
& \mid GP \\
& \text{(GP are “pure” ghost programs)} \\
& \mid \mathsf{skip} \mid \mathsf{assume}\ cond \mid \mathsf{return} \\
& \mid P\,;\,P \mid \mathsf{if}\ cond\ \mathsf{then}\ P\ \mathsf{else}\ P \mid \mathsf{while\ cond\ do}\ P \\
cond \coloneqq\ & BoolExpr[Var_U, \mathcal{F}] \\
GP \coloneqq\ & a \coloneqq Expr[Var_U \cup Var_G, \mathcal{F} \cup \mathcal{G}] \mid b \coloneqq x.g \mid b \coloneqq x.f \\
& \text{(Ghost variables can read from both user and ghost variables/maps)} \\
& \mid x.g \coloneqq b \mid x.g \coloneqq y \\
& \text{(Ghost maps can only be assigned values from ghost variables)} \\
& \mid \overline{s} \coloneqq GhostFunc(\overline{v}) \qquad \overline{s}, \overline{v} \text{ are variables in } Var_G,\ GhostFunc \text{ is } \textbf{always terminating} \\
& \mid \mathsf{skip} \mid GP\,;\,GP \mid \mathsf{if}\ Gcond\ \mathsf{then}\ GP\ \mathsf{else}\ GP \\
& \mid \mathsf{while\ Gcond\ do}\ GP \qquad \text{loop is } \textbf{always terminating} \\
Gcond \coloneqq\ & BoolExpr[Var_U \cup Var_G, \mathcal{F} \cup \mathcal{G}]
\end{aligned}
$$

Fig. 6. Grammar of programs with ghost code. x, y, z are user variables Var_U, a, b are ghost variables Var_G, $f \in \mathcal{F}$ is a user field, and $g \in \mathcal{G}$ is a ghost map. Notation $Expr[Vars, Maps]$ denotes expressions over the vocabulary given by variables $Vars$ and maps $Maps$, similarly $BoolExpr[Vars, Maps]$ denotes boolean expressions. Termination for ghost loops and functions can be established in any way.

In this section we formally define our programming language augmented with ghost code, as well as the projection of ghost-augmented code to ‘user’ code.

Fix a set of user variables Var_U and ghost variables Var_G. We already introduced user fields $\mathcal{F}$ and ghost fields/maps $\mathcal{G}$ in Section 2.2. We define a programming language over this vocabulary in Figure 6 below. The main aspects to note are: (a) ghost variables can read from user variables/maps, but the reverse is not allowed, (b) ghost conditionals and loops must only contain bodies that are purely ghost, and (c) ghost loops and functions must always terminate. These choices ensure that

ghost variables do not affect the execution of the user program. We can formalize this claim using the idea of 'projecting out' ghost code and obtaining a pure user program

Projection that Eliminates Ghost Code. Fix a main method M with body Q_0. Let $N_i, 1 \leq i \leq k$ be a set of auxiliary methods with bodies Q_i that Q_0 can call. Note that the bodies Q_0 and Q_i contain ghost code. Let us denote a program containing these methods by $[(M : Q_0); (N_1 : Q_1) \ldots (N_k : Q_k)]$. We then define projection as follows:

Definition A.1 (Projection of Ghost-Augmented Code to User Code). The projection of the ghost-augmented program $[(M : Q_0); (N_1 : Q_1) \ldots (N_k : Q_k)]$ is the user program $[(\hat{M} : \hat{Q_0}); (\hat{N_1} : \hat{Q_1}) \ldots (\hat{N_k} : \hat{Q_k})]$ such that:

(1) The input (resp. output) signature of $\hat{M}$ is that of M with the ghost input (resp. output) parameters removed. Formally, given a sequence of parameters $\overline{x}$ with some elements in the sequence marked as ghost, we can define the projection as the sequence formed by the non-continguous subsequence of parameters in $\overline{x}$ consisting of non-ghost parameters.

(2) $\hat{Q_0}$ is derived from Q_0 by: (a) eliminating all ghost code, i.e., replacing yields of the nonterminal *GP* in Figure 6 with skip, and (b) replacing each non-ghost function call statement of the form $\overline{r} := N_j(\overline{t})$ with the statement $\overline{s} := \hat{N}_j(\overline{u})$, where $\overline{u}, \overline{s}$ are obtained from $\overline{t}, \overline{r}$ by projecting out the elements corresponding to the ghost parameters in the signature of N_j. Each $\hat{Q_i}$ is derived from the corresponding Q_i by a similar transformation.
We define this formally as a recursive transformation on the structure of the grammar of P (ghost-augmented programs) in Figure 6:

$$
\begin{aligned}
\mathit{Projection}(GP) &= \text{skip} \\
\mathit{Projection}(\overline{r} := \mathit{Func}(\overline{t})) &= \overline{s} := \hat{\mathit{Func}}(\overline{u}) \\
&\quad\ \overline{u}, \overline{s} \text{ are obtained from } \overline{t}, \overline{r} \text{ by projecting out} \\
&\quad\ \text{elements corresponding to ghost parameters} \\
\mathit{Projection}(\text{stmt}) &= \text{stmt} \quad \text{for all other statements} \\
\mathit{Projection}(P_1;\, P_2) &= \mathit{Projection}(P_1)\,;\, \mathit{Projection}(P_2) \\
\mathit{Projection}(\text{if } \mathit{cond} \text{ then } P_1 \text{ else } P_2) &= \text{if } \mathit{cond} \text{ then } \mathit{Projection}(P_1) \text{ else } \mathit{Projection}(P_2) \\
\mathit{Projection}(\text{while cond do } P) &= \text{while cond do } \mathit{Projection}(P)
\end{aligned}
$$

A.3 Generating Quantifier-Free Verification Conditions

The FWYB methodology described in previous sections shows that we can soundly reduce the problem of verifying programs with intrinsic specifications to the problem of verifying programs (with ghost code) with quantifier-free contracts. We then argue that we can reason with the latter using combinations of various quantifier-free theories including sets and maps with pointwise updates. In this section we detail some subtleties involved in the argument.

It is well-known that we can reason with scalar programs with quantifier-free contracts by generating quantifier-free verification conditions (which in turn can be handled by SMT solvers). However, this is not immediately clear for programs that dynamically manipulate heaps. In particular, commands such as allocation and function calls pose challenges in formulating quantifier-free verification conditions.

At a high level, our solution transforms the given heap program into a scalar program that explicitly encodes changes to the heap. Specifically, we show an encoding for the field mutation, allocation, and function call statements.

Modeling Field Mutation. As described earlier, we model the monadic maps and fields as updatable maps [19]. Formally, we introduce a map M_f (also called an *array* in SMT solvers like Z3 [18]) for every field/monadic map f. We then encode the commands for field lookup and mutation as map operations. For example, the mutation $x.f := y$ is encoded as $M_f[x] := y$.

Modeling Allocation. We model programs in a safe garbage-collected programming language. We introduce a ghost global variable *Alloc* to model the allocated set of objects in the program. We then add several assumptions (i.e., assume statements) throughout the program. Specifically, we assume for every program parameter of type Object, the parameter itself as well as the values of the monadic maps of type Object/Set-of-Objects on the parameter are all contained in *Alloc*. For example, in the case of our running example (Example 3.6), we add the assumptions $x \in Alloc$ and $next(x) \neq nil \implies next(x) \in Alloc$. If we had a monadic map *hslist* corresponding to the heaplet of the sorted list, we would also add the assumption $hslist(x) \subseteq Alloc$. Similarly, whenever an object is dereferenced on a field of type Object/Set-of-Objects in the program, we add an assumption that the resulting value is contained in *Alloc*. Note that these are quantifier-free assumptions. They can be added soundly since they are valid under the semantics of the underlying language.

We then model allocation by introducing a new object to *Alloc* and ensure that the default values of the various fields on the newly allocated object belong to *Alloc*. These constraints can be expressed using a quantifier-free formula over maps.

Modeling Heap Change Across Function Calls. The main challenge in modeling function calls is to ensure the ability to do frame reasoning. To do this, we extend the programming language with a *modified set* annotation for methods. We require the modified set to be a term of type Set-of-Objects that is constructed using object variables in the current scope and monadic maps over them. In the case of our running example (Example 3.1), we would add a monadic map *hslist* of type Set-of-Objects corresponding to the heaplet of the sorted list and annotate the program with $hslist(x)$ as the modified set. Figure 7 shows the full version of sorted list insertion with the modified set annotation.

Given a modified set *Mod*, we model changes to the heap across a function call by introducing new maps corresponding to the various fields (including monadic maps) after the call. We then add assumptions that the values of the new maps are equal to the values of the maps before the call on all locations that do not belong to the modified set *Mod*. Although this constrains the maps on unboundedly many objects, it can be written without quantifiers by using pointwise operators on maps [19]. Formally, for a field f modeled as a map M_f, we introduce a new map M'_f and update M_f as:

$$M_f[x] := ite(x \in Mod,\ M'_f[x],\ M_f[x])$$

The above update can be expressed using pointwise operators as $M_f := ite(Mod, M'_f, M_f)$, where the *ite* operator is applied pointwise over the maps *Mod*, M_f, and M'_f. The value of the field f on an object x after the call will then be equal to $x.f$ before the call if x was not modified, and a *havoc*-ed value given by M'_f otherwise. Pointwise operators are supported by the generalized array theory [19] whose quantifier-free fragment is decidable.

Program verifiers like Boogie [55] offer VC generation frameworks that are amenable to the modeling described in this section. Indeed, our implementation of the IDS/FWYB methodology described in Section 5.1 uses Boogie.

B PROOFS OF SOUNDNESS FOR STAGES 1, 2, AND 3 OF FWYB

In this section we detail the proofs of soundness for the various stages of the FWYB methodology. We first introduce some notation and show some preliminary lemmas.

Projection for Configurations. The stages of FWYB deal with two kinds of triples, one whose validity is stated with respect to configurations that interpret ghost variables and maps, and one over configurations that only interpret user variables and fields. Given a configuration C that interprets ghost variables/maps, we denote by $\hat{C}$ the projection of that configuration to user variables that simply eliminates all ghost interpretations. Conversely, given a configuration c we say that C extends c with an interpretation for ghost variables/maps if $\hat{C} = c$. We define $\hat{bot} = \bot$.

Lemmas About Projection that Eliminates Ghost Code. We show the following lemmas about projection that eliminates ghost code (Definition A.1). We assume that there is only one procedure M in the program for simplicity of presentation. Recall that M can contain ghost code and $\hat{M}$ is the projection of M that eliminates the ghost code (with appropriately modified input/output parameters).

Lemma B.1. *Let C_1 be a configuration that interprets ghost variables/maps. If M is a "pure ghost" program, (i.e., a yield of GP in the grammar in Figure 6), then M always terminates starting from C_1.*

The above lemma says that pure ghost programs always terminate. It follows directly from the definition of ghost code which requires pure ghost loops and functions to be terminating. □

Lemma B.2. *Let c be a configuration that does not interpret ghost variables/maps. If $\hat{M}$ (projected code that does not contain ghost code) terminates starting from c, then M (which contains additional ghost code) must terminate starting from any configuration C that extends c.*

The above lemma says that the termination of the original user program is preserved by any augmentation with ghost code. In a certain sense, it 'lifts' Lemma B.1 to programs that contain both user and ghost code.

Proof. The lemma follows from structural induction on the definition of projection, i.e., on the structure of the grammar for the nonterminal P in Figure 6. The argument for basic statements is trivial. For pure ghost programs the result follows from Lemma B.1. The argument for all compositions (sequential, conditional, loop) and function calls follows from the induction hypothesis. □

Lemma B.3. *Let C_1 be a configuration that interprets ghost variables/maps. If M is a "pure ghost" program and M starting from C_1 reaches some C_2 and $C_2 \neq \bot$, then $\hat{C}_1 = \hat{C}_2$.*

The above lemma says that ghost code does not affect the values of user (non-ghost) variables and maps. It follows trivially by structural induction on the *GP* grammar, using the definition of operational semantics (Figure 5). The key observation is that *GP* syntactically disallows non-ghost variables/maps to read from ghost variables/maps. □

We can similarly 'lift' the above lemma to programs that contain both user code and ghost code.

Lemma B.4. *Let c be a configuration that does not interpret ghost variables/maps. If $\hat{M}$ starting from c_1 reaches some c_2, then M starting from any configuration C_1 that extends c_1 must either reach $\bot$ or some C_2 that extends c_2.*

The above lemma says that augmentation with ghost code does not affect how the original program executes.

Proof. As with Lemma B.2, we proceed by structural induction on the grammar for P in Figure 6. The argument for basic non-ghost statements follows trivially from the definition of operational semantics. They key observation is that non-ghost statements do not affect the values of ghost variables/maps (ensured by the syntactic restrictions). For pure ghost programs the result follows

from Lemma B.3. The argument for all compositions (sequential, conditional, loop) and function calls follows from the induction hypothesis. □

PROOF OF PROPOSITION 3.4

We can state the proposition simply as follows: if $\langle\, LC \wedge \psi_{pre} \,\rangle\ M\ \langle\, LC \wedge \psi_{post} \,\rangle$ is valid, then $\langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre} \,\rangle\ \hat{M}\ \langle\, \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post} \,\rangle$ is valid.

Fix configurations (without ghost state) c_1, c_2 such that c_1 satisfies $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre}$ and $\hat{M}$ starting from c_1 reaches c_2. To show that the given Hoare triple for $\hat{M}$ is valid, we must establish that c_2 is not $\bot$, and further that c_2 satisfies $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$.

Since $c_1 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{pre}$, by the semantics of second order logic there exists a configuration (taken as a model) extending c_1, say C_1, such that $C_1 \models LC \wedge \psi_{pre}$. First, using Lemma B.2 we have that M starting from C_1 must terminate. Further, since the triple $\langle\, LC \wedge \psi_{pre} \,\rangle\ M\ \langle\, LC \wedge \psi_{post} \,\rangle$ is valid, it must be the case that M starting from C_1 reaches some C_2 such that $C_2 \neq \bot$ and $C_2 \models LC \wedge \psi_{post}$.

We now use Lemma B.4 to conclude that $\hat{C_2} = c_2$. Since $C_2 \neq \bot$, we have that $c_2 \neq \bot$. Further, since $C_2 \models LC \wedge \psi_{post}$, we have from the semantics of the logic that $C_2 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$.

Observe that the formula $\exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$ is stated over the common vocabulary of C_2 and c_2, where the interpretations of the two configurations agree. Therefore, we can conclude that $c_2 \models \exists g_1, g_2 \ldots, g_k.\, LC \wedge \psi_{post}$. This concludes the proof. □

Proof of Proposition 3.5

The proof of Proposition 3.5 is similar to the above proof for Proposition 3.4, except that we must now consider a definition of ghost code (akin to the development in Section A) that only considers the variable Br as ghost.

Repeating the arguments in the proof of Proposition 3.4 appropriately, we obtain that if

$$\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha \wedge Br = \emptyset \,\rangle\ P_{\mathcal{G},Br}(\overline{x}, Br,\ ret\colon\ \overline{y}, Br)\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta \wedge Br = \emptyset \,\rangle$$

is valid, then

$\langle\, \exists Br.\, \big((\forall z \notin Br.\, \rho(z)) \wedge \alpha \wedge Br = \emptyset\big) \,\rangle\ P_{\mathcal{G}}(\overline{x},\ ret\colon\ \overline{y})\ \langle\, \exists Br.\, \big((\forall z \notin Br.\, \rho(z)) \wedge \beta \wedge Br = \emptyset\big) \,\rangle$
is valid. This triple can be simplified to $\langle\, (\forall z.\, \rho(z)) \wedge \alpha \,\rangle\ P_{\mathcal{G}}(\overline{x},\ ret\colon\ \overline{y})\ \langle\, (\forall z.\, \rho(z)) \wedge \beta \,\rangle$, which concludes the proof.

PROOF OF PROPOSITION 3.7

Given a well-behaved program P such that $\{\alpha\}\ P\ \{\beta\}$ is valid, we must show that $\langle\, (\forall z \notin Br.\, \rho(z)) \wedge \alpha \,\rangle\ P\ \langle\, (\forall z \notin Br.\, \rho(z)) \wedge \beta \,\rangle$ is valid.

The proof proceeds by an induction on the nesting depth of method calls in a trace of the program P. We elide this level of induction here because it is routine. Importantly, given a particular execution of the program P, we must show that the claim holds, assuming it holds for all method calls occurring in the execution. We show this by structural induction on the proof of well-behavedness of P.

There are several base cases.

Skip/Assignment/Lookup/Return There is nothing to show for skip, assignment, lookup, or return statements. These do not change the heap at all and the rule does not update Br either, therefore if $\langle\, \alpha \,\rangle$ stmt $\langle\, \beta \,\rangle$ is valid then certainly $\langle\, (\forall z \notin Br.\, \rho) \wedge \alpha \,\rangle$ stmt $\langle\, (\forall z \notin Br.\, \rho) \wedge \beta \,\rangle$ is valid.

Mutation The claim is true for the mutation rule since by the premise of the rule we update the broken set with the impact set consisting of all potential objects where local conditions may not hold.

Function Call Here we simply appeal to the induction hypothesis.

Allocation We refer to our operational semantics, which ensures that no object points to a freshly allocated object. Therefore, the allocation of an object could have only broken the local conditions on itself at most.

Infer LC Outside Br There is nothing to prove for this rule as it does not alter the Br set at all.

Assert LC and Remove The claim holds for this rule by construction. If LC holds everywhere outside Br , and we know that $LC(x)$ holds, then we can conclude that LC holds everywhere outside $Br \setminus \{x\}$.

It only remains to show that the claim holds for larger well-behaved programs obtained by composing smaller well-behaved programs using sequencing, branching, or looping constructs. The proof here is trivial as the argument for sequencing is trivial (we can think of a loop as unboundedly many sequenced compositions of the smaller well-behaved program): we can *always* compose two well-behaved programs to obtain a well-behaved program. □

C DETAILS FOR WELL-BEHAVED PROGRAMMING

General Construction for Automatically Checking Correctness of Impact Sets

Fix a class with maps $\mathcal{F} \cup \mathcal{G} = \{f_1, f_2, \ldots f_n\}$ (includes both original and ghost fields) and an intrinsic definition $(\mathcal{G}, LC, \varphi)$ over which we prove correctness of programs. Without loss of generality, let $f_1, \ldots f_k$ for some $k \leq n$ alone correspond to pointer fields (where the range is an object); the others we assume are data fields that range over background sorts. In the sequel we assume for simplicity that $LC(x)$ only relates the fields of x with those of $f_i(x)$ for $1 \leq i \leq k$, i.e., the local conditions only constrain the fields of x with those of its neighboring objects that are "one pointer hop" away from x.

Consider a mutation $x.f := y$ for some f in f_1 through f_n and an arbitrary y. It is clear that the only set of objects whose local condition can be impacted by this mutation are those that are one pointer hop away via an incoming or outgoing edge in the heap (seen as a directed graph with labeled edges corresponding to pointers), apart from x itself. In general there can be unboundedly many such objects, but in our work we only handle impact sets that can be expressed as a finite set of terms over x (see Section 3.5 under 'Rules for Constructing Well-Behaved Programs'). Note here that the impact set can be larger than the set of impacted objects as we only require that objects not belonging to the impact set retain that LC holds on them under mutation. However, we attempt here to construct of impact sets that are as small as possible.

Following the above discussion, let us assume that the impact set consists of terms from the following set:

$$ImpactableObjects = \{x, f_1(x), \ldots f_k(x)\} \cup \{old(f(x)) \mid f \text{ is a pointer field}\}$$

The reader may be inclined to suggest here that when f is a pointer field, y (the new value of $f(x)$) may also belong to the minimal impact set. However, this is not possible in general since y is arbitrary, and in particular y can be an object in the heap that is "far away" from x, i.e., not one pointer hop away (either incoming or outgoing). The same argument applies to terms over y. Therefore, if the (minimal) impact set is at all expressible as a set of terms over the vocabulary of the mutation statement it must be a subset of the terms in the set *ImpactableObjects* defined above.

Let this subset of terms be A. We then generate the following triple to check that A is in fact an impact set:

$$\vdash \{(\bigwedge_{t \in A} u \neq t) \wedge LC(u) \wedge x \neq nil\}\ x.f := y\ \{LC(u)\}$$

The triple says that any location u that is not A which satisfied LC before the mutation must continue to satisfy it after the mutation. As discussed in the main text, this validity of this triple

```
pre: Br = ∅
post: LC(r) ∧ prev(r) = nil
    ∧ Br = ite(old(prev(x)) = nil, ∅, {old(prev(x))})
    ∧ length(r) = old(length(x)) + 1
    ∧ keys(r) = old(keys(x)) ∪ {k}
    ∧ old(hslist(x)) ⊂ hslist(r)
modifies: hslist(x)
sorted_list_insert(x: C, k: Int, Br: Set(C))
returns r: C, Br: Set(C)
{
  InferLCOutsideBr(x, Br);
  if (x.key ≥ k) then { // k inserted before x
    NewObj(z, Br);          // {z}
    Mut(z, key, k, Br);    // {z} since z.prev = nil
    Mut(z, next, x, Br);   // {z} since z.next = nil
    Mut(z, hslist, {z} ∪ x.hslist, Br); // {z}
    Mut(z, length, 1 + x.length, Br);    // {z}
    Mut(z, keys,  {k} ∪ x.keys, Br);     // {z}
    Mut(x, prev, z, Br);     // {z, x, old(prev(x))}
    AssertLCAndRemove(z, Br);  // {x, old(prev(x))}
    AssertLCAndRemove(x, Br); // {old(prev(x))}
    r := z;
  }
  else {
  if (x.next = nil) then { // one-element list
    NewObj(z, Br);
    Mut(z, key, k, Br);
    Mut(z, next, nil, Br);
    Mut(z, hslist, {z}, Br);
    Mut(z, length, 1, Br);
    Mut(z, keys, {k}, Br);
    Mut(x, next, z, Br);
    Mut(z, prev, x, Br);
    AssertLCAndRemove(z, Br);
    Mut(x, prev, nil, Br);
    Mut(x, hslist, {x} ∪ {z}, Br);
    Mut(x, length, 2, Br);
    Mut(x, keys, {x.key} ∪ {k}, Br);
    AssertLCAndRemove(x, Br);
    r := x;
  }
  else { // recursive case
    y := x.next;
    InferLCOutsideBr(y, Br);
    tmp, Br := sorted_list_insert(y, k, Br);  // {x}
    InferLCOutsideBr(y, Br);
    if (y.prev = x) then {
      Mut(y, prev, nil, Br);    // {y, x}
    }
    Mut(x, next, tmp, Br);      // {y, x}
    AssertLCAndRemove(y, Br);   // {x}
    Mut(tmp, prev, x, Br);      // {tmp, x}
    AssertLCAndRemove(tmp, Br); // {x}
    Mut(x, hslist, {x} ∪ tmp.hslist, Br); // {x, prev(x)}
    Mut(x, length, 1 + tmp.length, Br);    // {x, prev(x)}
    Mut(x, keys, {x.key} ∪ tmp.keys, Br); // {x, prev(x)}
    Mut(x, prev, nil, Br);        // {x, old(prev(x))}
    AssertLCAndRemove(x, Br);   // {old(prev(x))}
    r := x;
  }}
}
```

Fig. 7. Code for insertion into a sorted list written in the syntactic fragment for well-behaved programs(Section 4.1). Black lines denote code written by the user, and blue lines denote lines written by the verification engineer. The comments on the right show the state of the broken set *Br* after the statement on the corresponding line.

can be check effectively by decision procedures over quantifier-free combinations of theories that are supported by SMT solvers [8, 18].

Finally, we can compute a provably correct and minimal impact set automatically, if one exists, by considering subsets of *ImpactableObjects* in turn and checking the corresponding triple as described above. However, in our experiments we compute impact sets manually and check their correctness automatically.

D DETAILS FOR CASE STUDIES IN SECTION 4

In this appendix we provide further details for the various case studies discussed in the main text and detail some other case studies not featured in the main text.

D.1 Discussion on Sorted List Insertion (Section 4.1)

We provide the specifications and the code augmented with ghost annotations in Figure 7.

Specifications. The precondition states that the broken set is empty at the beginning of the program. The postcondition states that the returned object r satisfies the local conditions and satisfies the correlation formula for a sorted list (i.e., $prev(r) = nil$). However, the broken set is only empty if the input object x was the head of a sorted list, and it is $\{prev(x)\}$ otherwise. The other conjuncts express functional specifications for insertion in terms of the length, heaplet, and set of keys. We also add a 'modifies' clause which enables program verifiers for heap manipulating programs to utilize frame reasoning across function calls.

Summary. The proof works at a high-level as follows: we recurse down the list, reaching the appropriate object x before which the new key must be inserted. This is the first branch in Figure 7, and we show the broken set at each point in the comments to the right. We create the new object z with the appropriate key and point $z.next$ to x. We then fix the local conditions on x and z. However, these fixes break the LC on $old(prev(x))$. We maintain this property up the recursion, at each point fixing LC on x and breaking it on $old(prev(x))$ in the process. This is shown in the last branch in the code. We eventually reach the head of the sorted list, whose *prev* in the pre state is *nil*, and at that point the fixes do not break anything else, i.e., the broken set is empty (as desired).

The verification engineer adds ghost code to perform these fixes as shown in blue in Figure 7. We can also see that there are essentially as many lines of ghost code as there are lines of user code; we compare these values across our benchmark suite (see Table 2) and find that this is typical for many methods. However, the verification conditions for the (augmented) program are *decidable* because they can be stated using quantifier-free formulas over decidable combinations of theories including maps, map updates, and sets.

D.2 BST Right-Rotation

We now turn to another data structure and method that illustrates intrinsic definitions for trees, namely verifying a right rotate on a binary search tree. Such an operation is a common tree operation, and rotations are used widely in maintaining balanced search trees, such as AVL and Red-Black Trees, on which several of our benchmarks operate.

We augment the definition of binary trees discussed in Section 1 to include the $min : BST \rightarrow Real$ and $max : BST \rightarrow Real$ maps, which capture the minimum and maximum keys stored in the tree rooted at a node, to help enforce binary search tree properties locally. The local condition and the impact sets are as below:

$$
\begin{aligned}
&LC \equiv \forall x.\, min(x) \leq key(x) \leq max(x) \\
&\wedge\ (p(x) \neq nil \Rightarrow l(p(x)) = x \vee r(p(x)) = x) \\
&\wedge\ (l(x) = nil \Rightarrow min(x) = key(x)) \\
&\wedge\ (l(x) \neq nil \Rightarrow p(l(x)) = x \wedge rank(l(x)) < rank(x) \\
&\qquad\qquad \wedge\ max(l(x)) < key(x)\ \wedge\ min(x) = min(l(x))) \\
&\wedge\ (r(x) = nil \Rightarrow max(x) = key(x)) \\
&\wedge\ (r(x) \neq nil \Rightarrow p(r(x)) = x \wedge rank(r(x)) < rank(x) \\
&\qquad\qquad \wedge\ min(r(x)) > key(x)\ \wedge\ max(x) = max(r(x)))
\end{aligned}
$$

Mutated Field f	Impacted Objects A_f
l	$\{x, old(l(x))\}$
r	$\{x, old(r(x))\}$
p	$\{x, old(p(x))\}$
key	$\{x\}$
min	$\{x, p(x)\}$
max	$\{x, p(x)\}$
$rank$	$\{x, p(x)\}$

We first describe the gist of how the data structure is repaired and provide the fully annotated program below. Recall that in a BST right rotation, that there are two nodes x and y such that y is x's left child. After the rotation is performed, y becomes the new root of the subtree, while x becomes y's right child. Several routine updates of the monadic map p (parent) will have to be made. The most interesting update is that of the $rank : BST \rightarrow Real$ map. Since y is now the root of the affected subtree, its rank must be greater than all its children. One way of doing this is to increase y's rank to something greater than x's rank. This works if y has no parent, but not in general. To solve this issue, we use the density of the Reals to set the rank of y to $(rank(x) + rank(p(y)))/2$. Note that there are a fixed number of ghost map updates, as the various monadic maps for distant ancestors and descendents of x, y do not change (the min/max of subtrees of such nodes do not change).

We present the fully annotated program below, with comments displaying the state of the broken set Br at the corresponding point in the program.

```
pre: Br = ∅ ∧ l(x) ≠ nil ∧ p(x) = xp
post: Br = ∅ ∧ p(ret) = xp
      ∧ l(ret) = old(l(l(x))) ∧ ret = old(l(x)) ∧ r(ret) = x
      ∧ l(r(ret)) = old(r(l(x))) ∧ r(r(ret)) = old(r(x))
bst_right_rotate(x: BST, xp: BST?, Br: Set(BST))
returns ret: BST, Br: Set(BST)
{
  LCOutsideBr(x, Br);
  if (xp ≠ nil) then {
       LCOutsideBr(xp, Br);
  }
  if (x.l ≠ nil) then {
       LCOutsideBr(x.l, Br);
  }
  if (x.l ≠ nil ∧ x.l.r ≠ nil) then {
       LCOutsideBr(x.l.r, Br);
  }
  var y := x.l;                // {}
  Mut(x, l, y.r, Br);          // {x, y}
  if (xp ≠ nil) then {
       if (x = xp.l) then {
           Mut(xp, l, y, Br);  // {xp, x, y}
       }
       else {
           Mut(xp, r, y, Br);  // {xp, x, y}
       }
  }
  Mut(y, r, x, Br);            // {xp, x, y, x.l} (Note: x.l == old(y.r))
  // (1): Repairing x.l
  if (x.l ≠ nil) then {
       Mut(x.l, p, x, Br);     // {xp, x, y, x.l}
  }
  // (2): Repairing x
  Mut(x, p, y, Br);            // {xp, x, y, x.l}
  Mut(x, min, if x = nil then x.k else x.l.min, Br);    // {xp, x, y, x.l}
  // (3): Repairing y
  Mut(y, p, xp, Br);           // {xp, x, y, x.l}
  Mut(y, max, x.max, Br);      // {xp, x, y, x.l}
  Mut(y, rank, if xp = nil then x.rank+1 else (xp.rank+x.rank)/2, Br);     // {xp, x, y, x.l}
  AssertLCAndRemove(x.l, Br);  // {xp, x, y}
  AssertLCAndRemove(x, Br);    // {xp, y}
  AssertLCAndRemove(y, Br);    // {xp}
  AssertLCAndRemove(xp, Br);   // {}
  ret := y // return y
}
```

D.3 Discussion on Sorted List Reversal (Section 4.2)

What follows are the complete local conditions and impact sets for Sorted List Reverse:

The following program reverses a sorted list as defined by the local condition above. We annotate this program with comments on the current composition of the broken set according to the rules of Table 3.

```
pre: Br = ∅ ∧ φ(x) ∧ sorted(x)
post: Br' = ∅ ∧ φ(ret) ∧ rev_sorted(ret) ∧ keys(ret) = old(keys(x)) ∧ hslist(ret) = old(hslist(x))
sorted_list_reverse(x: C, Br: Set(C))
returns ret: C, Br: Set(C)
{
```

$$
\begin{aligned}
LC \equiv \forall x.\, prev(x) \neq nil \Rightarrow next(prev(x)) = x \\
\wedge\ next(x) \neq nil \Rightarrow prev(next(x)) = x \\
\wedge\ length(x) = length(next(x)) + 1 \\
\wedge\ keys(x) = keys(next(x)) \cup \{key(x)\} \\
\wedge\ hslist(x) = hslist(next(x)) \uplus \{x\} \\
\wedge\ sorted(x) \Rightarrow key(x) \leq key(next(x)) \\
\wedge\ sorted(x) = sorted(next(x)) \\
\wedge\ rev\_sorted(x) \Rightarrow key(x) \geq key(next(x)) \\
\wedge\ rev\_sorted(x) = rev\_sorted(next(x)) \\
\wedge\ (next(x) = nil \Rightarrow length(x) = 1 \wedge keys(x) = \{x\} \wedge hslist(x) = \{x\})
\end{aligned}
$$

Fig. 9. Full local condition for lists for Sorted List Reverse

Mutated Field f	Impacted Objects A_f
$next$	$\{x, old(next(x))\}$
key	$\{x, prev(x)\}$
$prev$	$\{x, old(prev(x))\}$
$length$	$\{x, prev(x)\}$
$keys$	$\{x, prev(x)\}$
$hslist$	$\{x, prev(x)\}$
$sorted$	$\{x, prev(x)\}$
rev_sorted	$\{x, prev(x)\}$

Table 3. Full impact sets for lists for Sorted List Reverse

```
LCOutsideBr(x, Br);
var cur := x;
ret := null;
while (cur ≠ nil)
   invariant cur ≠ nil  ⇒  LC(cur) ∧ sorted(cur) ∧ φ(cur)
   invariant ret ≠ nil  ⇒  LC(ret) ∧ rev_sorted(ret) ∧ φ(ret)
   invariant cur ≠ nil  ∧  ret ≠ nil  ⇒  key(ret)  ≤  key(cur)
   invariant old(keys(x)) = ite(cur = nil,  ∅,  keys(cur)) ∪ ite(ret = nil,  ∅,  keys(ret))
   invariant old(hslist(x)) = ite(cur = nil,  ∅,  hslist(cur)) ∪ ite(ret = nil,  ∅,  hslist(ret))
   invariant Br = ∅
   decreases ite(cur ≠ nil,  0,  length(cur))
{
   var tmp := cur.next;                  // {}
   if (tmp ≠ nil) then {
     LCOutsideBr(tmp, Br);               // {}
     Mut(tmp, p, nil, Br);               // {cur, tmp}
   }
   Mut(cur, next, ret, Br);              // {cur, tmp}
   if (ret ≠ nil) then {
     Mut(ret, p, cur, Br);               // {cur, tmp, ret}
   }
   Mut(cur, keys,
```

```
      {cur.k} ∪ (if cur.next=nil then φ else cur.next.keys), Br);    // {cur, tmp, ret}
    Mut(cur, hslist,
      {cur} ∪ (if cur.next=nil then φ else cur.next.hslist), Br);    // {cur, tmp, ret}
    if (cur.next ≠ nil ∧ (cur.key > cur.next.key ∨ ¬cur.next.sorted)) {
      Mut(cur, sorted, false, Br);      // {cur, tmp, ret}
    }
    Mut(cur, rev_sorted, true, Br);     // {cur, tmp, ret}
    AssertLCAndRemove(cur, Br);         // {tmp, ret}
    AssertLCAndRemove(ret, Br);         // {tmp}
    AssertLCAndRemove(tmp, Br);         // {}
    ret := cur;
    cur := tmp;
  }
  // The current value of ret is returned
}
```

D.4 Discussion on Circular List Insert Back (Section 4.3)

We first provide the local conditions and impact sets for circular lists.

$$
\begin{aligned}
LC \equiv \forall x.\ & next(x) \neq nil \ \wedge\ prev(x) \neq nil \\
& \wedge\ next(prev(x)) \ =\ x \ \wedge\ prev(next(x)) \ =\ x \\
& \wedge\ last(x) = x \implies length(x) = 0 \wedge rev\_length(x) = 0 \\
& \qquad \wedge\ last(x) = last(next(x)) \\
& \qquad \wedge\ next(x) = x \implies keys(x) = \emptyset \wedge hslist(x) = \{x\} \\
& \qquad \wedge\ next(x) \neq x \implies keys(x) = keys(next(x)) && \text{(C1)} \\
& \qquad\qquad \wedge\ hslist(x) = \{x\} \cup hslist(next(x)) && \text{(C2)} \\
& \wedge\ last(x) \neq x \implies length(x) = length(next(x)) + 1 \\
& \qquad \wedge\ rev\_length(x) = rev\_length(prev(x)) + 1 \\
& \qquad \wedge\ next(x) = last(x) \implies keys(x) = \{key(x)\} \wedge hslist(x) = \{x\} \\
& \qquad \wedge\ next(x) \neq last(x) \implies keys(x) = \{key(x)\} \cup keys(next(x)) && \text{(C3)} \\
& \qquad\qquad \wedge\ hslist(x) = \{x\} \cup hslist(next(x)) && \text{(C4)} \\
& \qquad\qquad \wedge\ x \notin hslist(next(x)) \\
& \qquad \wedge\ last(x) = last(next(x)) \\
& \qquad \wedge\ last(last(x)) = last(x) \\
& \qquad \wedge\ x \in hslist(last(x)) \\
& \qquad \wedge\ prev(x) \in hslist(last(x)) \\
& \qquad \wedge\ next(x) \in hslist(last(x))
\end{aligned}
$$

Fig. 10. Full local condition for lists for Circular List Insert Back

The local condition LC for circular lists can be seen in Figure 10. For use in loop invariants, we have defined two variants of the local condition. One of these variants is $LC_{MinusNode}(x, n)$, which can be seen as a predicate on nodes x and n, and is formed from LC by replacing the clauses (C1), (C3), and (C4) in Figure 10 with the three clauses (C1'), (C3'), and (C4') in Figure 11. Additionally,

we have another variant: $LC_{Last}(x, n)$, which is formed from LC by replacing the clause (C2) in Figure 10 with $hslist(x) = \{x, n\} \cup hslist(next(x))$.

$$(keys(x) = keys(next(x)) \setminus \{key(n)\} \vee keys(x) = keys(next(x))) \quad \text{(C1')}$$
$$(keys(x) = (key(x) \cup keys(next(x))) \setminus \{key(n)\} \quad \text{(C3')}$$
$$\vee\ keys(x) = (key(x) \cup keys(next(x))))$$
$$(hslist(x) = (x \cup hslist(next(x))) \setminus \{n\}) \quad \text{(C4')}$$

Fig. 11. Alterations to Figure 10 to form $LC_{MinusNode}$

Note that in this example as well as other benchmarks where we introduce scaffolding nodes, in order to prove a bound on the impact set, we require that a precondition ϕ holds before we mutate particular fields of nodes. The fields, preconditions, and impact sets for every node can be seen in Table 4. Note that our benchmark contains another manipulation macro, `AddToLastHsList(x, n, Br)`, which, if `x` is a scaffolding node (or $last(x) = x$), adds the node `n` to the set $hslist(x)$. The precondition for invoking this macro is that $last(x) = x$, and the only object impacted by the macro is $\{x\}$.

Mutated Field f	Mutation Precond. ϕ	Impacted Objects A_f
$next$	$\top$	$\{x, old(next(x))\}$
key	$\top$	$\{x, prev(x)\}$
$prev$	$\top$	$\{x, old(prev(x))\}$
$last$	$last(x) \neq x \vee (last(x) = x \wedge hslist(x) = \{x\})$	$\{x, prev(x)\}$
$length$	$\top$	$\{x, prev(x)\}$
rev_length	$\top$	$\{x, next(x)\}$
$keys$	$\top$	$\{x, prev(x)\}$
$hslist$	$last(x) \neq x \vee (last(x) = x \wedge hslist(x) = \{x\})$	$\{x, prev(x)\}$

Table 4. Full impact sets for lists for Circular List Insert Back

We give the specification and program for Circular List Insert Back below.

```
pre: Br = ∅ ∧ next(x) = last(x)
post: Br = ∅ ∧ next(ret) = last(ret) ∧ last(ret) = old(last(x))
      ∧ keys(last(ret)) = old(keys(last(x))) ∪ {k} ∧ fresh(hslist(last(ret)) \ old(hslist(last(x))))
circular_list_insert_back(x: C, k: Int Br: Set(C))
returns ret: C, Br: Set(C)
{
  LCOutsideBr(x, Br);
  LCOutsideBr(x.next, Br);
  LCOutsideBr(x.prev, Br);

  var last: C = x.next;
  var node: C;
  NewObj(node, Br);
  Mut(node, key, k, Br);
  Mut(node, next, x.next, Br);
  Mut(x, next, node, Br);

  AddToLastHsList(last, node, Br);
  Mut(last, prev, node, Br);
  Mut(node, prev, x, Br);
  Mut(node, length, 1, Br);
  Mut(node, rev_length, 1 + node.prev.rev_length, Br);
  Mut(node, keys, {k}, Br);
  Mut(node, hslist, {node}, Br);
  Mut(node, last, node.prev.last, Br);
  AssertLCAndRemove(node, Br);

  ghost var cur: C = x;
  label PreLoop:
  while (cur ≠ last)
     invariant cur ≠ last ⇒
               Br = {cur, last}
               ∧ LC_MinusNode(cur, node)
               ∧ last(cur) = last
               ∧ LC_Last(last, node)
     invariant cur = last ⇒ LC_MinusNode(cur, node)
     invariant node ∈ hslist(next(cur))
     invariant k ∈ keys(next(cur))
     invariant Unchanged@PreLoop(node)
     invariant Unchanged@PreLoop(last)
     invariant Br ⊆ {cur, last}
     decreases rev_length(cur)
  {
     if (cur.prev ≠ last) {
       LCOutsideBr(cur.prev, Br);
     }
     Mut(cur, length, cur.next.length + 1, Br);
     Mut(cur, hslist, cur.next.hslist + {node});
     Mut(cur, keys, cur.next.keys + {node.k});
     AssertLCAndRemove(cur, Br);
     cur := cur.prev;
  }

  LCOutsideBr(node, Br);
  Mut(cur, keys, cur.next.keys, Br);
  AssertLCAndRemove(cur, Br);
```

```
  AssertLCAndRemove(node, Br);

  ret := node;
}
```

D.5 Merging Sorted Lists

We demonstrate the ability of intrinsic definitions to handle multiple data structures at once, using the example of in-place merging of two sorted lists. The method merges the two lists by reusing the two lists' elements, which is a natural pattern for imperative code. Once again, we extend the definition of sorted lists from Case Study 4.1. We add the predicates $list1 : C \rightarrow Bool$, $list2 : C \rightarrow Bool$, and $list3 : C \rightarrow Bool$, to indicate disjoint classes of lists. The relevant local condition and impact sets are:

$$
\begin{aligned}
&(list1(x) \lor list2(x) \lor list3(x)) \\
\land\ &\neg(list1(x) \land list2(x)) \land \neg(list2(x) \land list3(x)) \\
\land\ &\neg(list1(x) \land list3(x)) \\
\land\ &(list1(x) \Rightarrow (next(x) \neq nil \Rightarrow list1(next(x)))) \\
\land\ &(list2(x) \Rightarrow (next(x) \neq nil \Rightarrow list2(next(x)))) \\
\land\ &(list3(x) \Rightarrow (next(x) \neq nil \Rightarrow list3(next(x))))
\end{aligned}
$$

Mutated Field f	Impacted Objects A_f
$list1$	$\{x, prev(x)\}$
$list2$	$\{x, prev(x)\}$
$list3$	$\{x, prev(x)\}$

Disjointness of the three lists is ensured by insisting that every object has at most one of the three list predicates hold.

We give a gist of the proof of the merge method. The recursive program compares the keys at the heads of the first and second sorted lists, and adds the appropriate node to the front of the third list. It turns out that we can easily update the ghost maps for this node (making it belong to the third list, and updating its parent pointer and key set) as well as updating the parent pointer of the head of the list where the node is removed from. When one of the lists is empty, we append the third list to the non-empty list using a single pointer mutation and then, using a ghost loop, we update the nodes in the appended list to make $list3$ true (this needs a loop invariant involving the broken set).

We provide below the full local conditions and impact sets.

$$
\begin{aligned}
LC \equiv \forall x.(&list1(x) \vee list2(x) \vee list3(x)) \\
&\wedge \neg(list1(x) \wedge list2(x)) \wedge \neg(list2(x) \wedge list3(x)) \\
&\wedge \neg(list1(x) \wedge list3(x)) \\
&\wedge (prev(x) \neq nil \implies next(prev(x)) = x) \\
&\wedge (next(x) \neq nil \implies prev(next(x)) = x \\
&\qquad \wedge\ length(x) = length(next(x)) + 1 \\
&\qquad \wedge\ keys(x) = keys(next(x)) \cup \{key(x)\} \\
&\qquad \wedge\ hslist(x) = hslist(next(x)) \uplus \{x\} \quad \text{(disjoint union)} \\
&\qquad \wedge\ key(x) \leq key(next(x))) \\
&\wedge (next(x) = nil \implies length(x) = 1 \wedge keys(x) = \{key(x)\} \wedge hslist(x) = \{x\}) \\
&\wedge (list1(x) \implies (next(x) \neq nil \implies list1(next(x)))) \\
&\wedge (list2(x) \implies (next(x) \neq nil \implies list2(next(x)))) \\
&\wedge (list3(x) \implies (next(x) \neq nil \implies list3(next(x))))
\end{aligned}
\tag{3}
$$

Fig. 13. Full local condition for lists for Sorted List Reverse

Note that we also have a variation of the local condition LC_{NC}, used in ghost loop invariants, which is similar to Equation 3, except the final three conjuncts (those enforcing closure on $list1, list2, list3$) are removed. This is done when converting an entire list from one class to another (i.e., converting from $list1$ to $list3$). The following are the full impact sets for all fields of this data structure.

Mutated Field f	Impacted Objects A_f
$next$	$\{x, old(next(x))\}$
key	$\{x, prev(x)\}$
$prev$	$\{x, old(prev(x))\}$
$length$	$\{x, prev(x)\}$
$keys$	$\{x, prev(x)\}$
$hslist$	$\{x, prev(x)\}$
$list1$	$\{x, prev(x)\}$
$list2$	$\{x, prev(x)\}$
$list3$	$\{x, prev(x)\}$

Fig. 14. Full impact sets for disjoint sorted lists